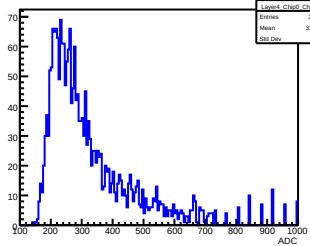
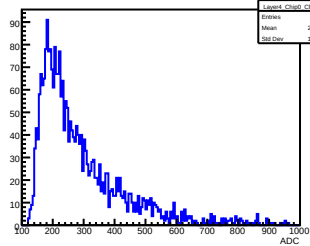


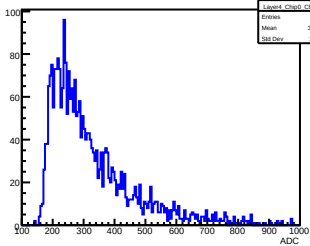
Layer 4, Chip 0, Channel 28



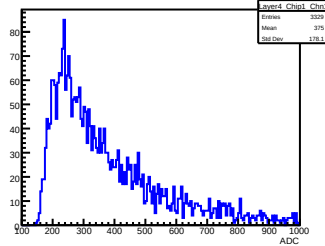
Layer 4, Chip 0, Channel 30



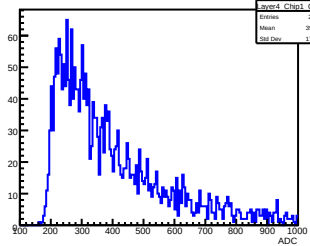
Layer 4, Chip 0, Channel 32



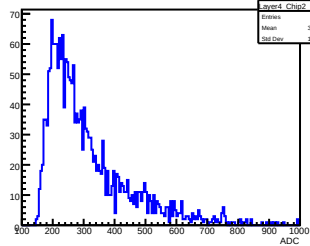
Layer 4, Chip 1, Channel 1



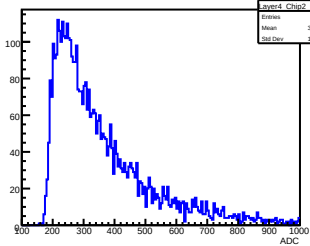
Layer 4, Chip 1, Channel 3



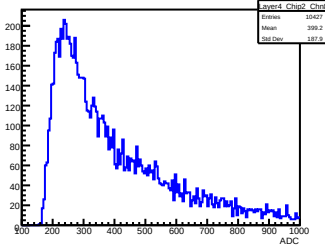
Layer 4, Chip 2, Channel 6



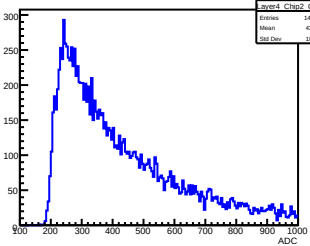
Layer 4, Chip 2, Channel 7



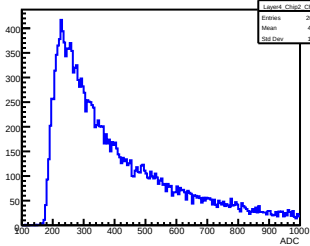
Layer 4, Chip 2, Channel 8



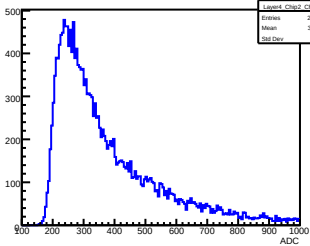
Layer 4, Chip 2, Channel 9



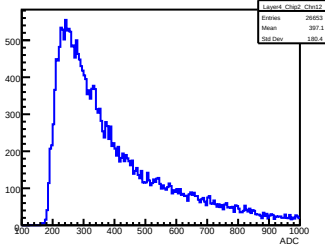
Layer 4, Chip 2, Channel 10



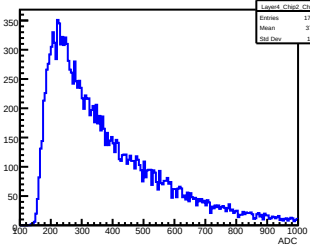
Layer 4, Chip 2, Channel 11



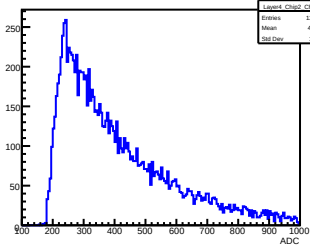
Layer 4, Chip 2, Channel 12



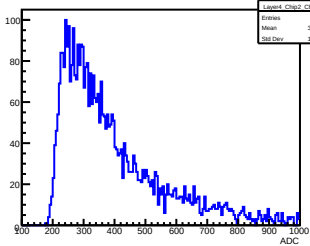
Layer 4, Chip 2, Channel 13



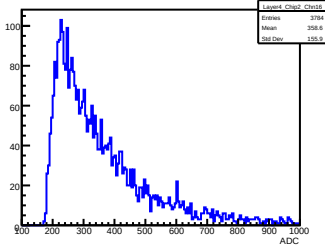
Layer 4, Chip 2, Channel 14



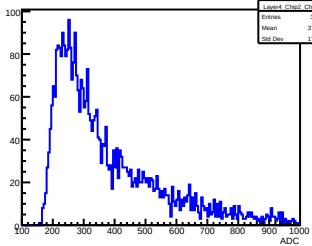
Layer 4, Chip 2, Channel 15



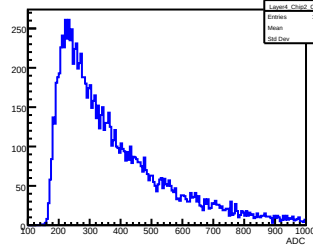
Layer 4, Chip 2, Channel 16



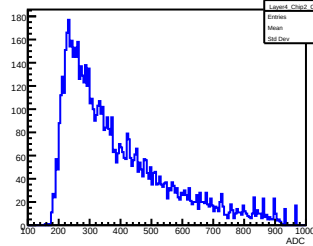
Layer 4, Chip 2, Channel 17



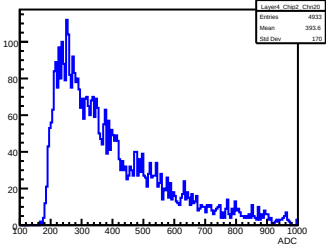
Layer 4, Chip 2, Channel 18



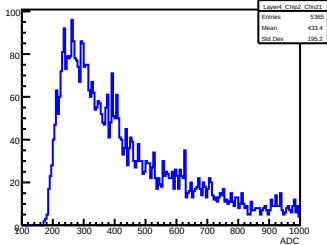
Layer 4, Chip 2, Channel 19



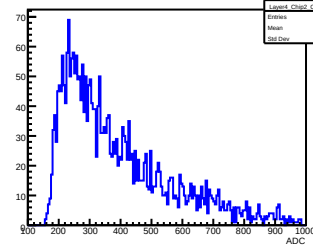
Layer 4, Chip 2, Channel 20



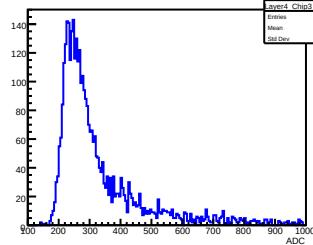
Layer 4, Chip 2, Channel 21



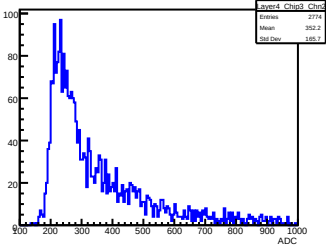
Layer 4, Chip 2, Channel 22



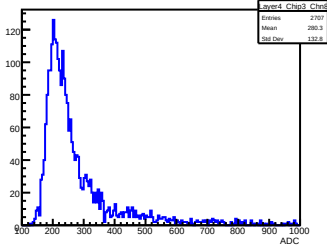
Layer 4, Chip 3, Channel 0



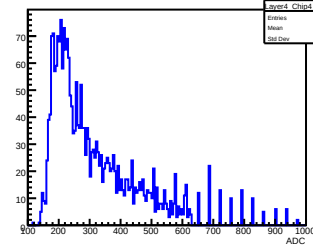
Layer 4, Chip 3, Channel 2



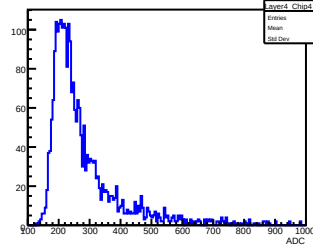
Layer 4, Chip 3, Channel 8



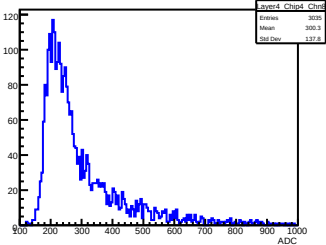
Layer 4, Chip 4, Channel 4



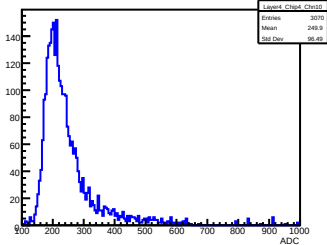
Layer 4, Chip 4, Channel 6



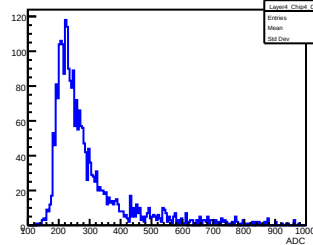
Layer 4, Chip 4, Channel 8



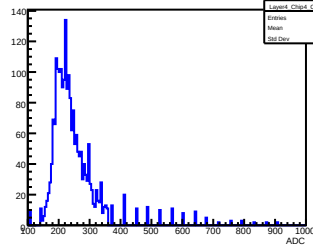
Layer 4, Chip 4, Channel 10



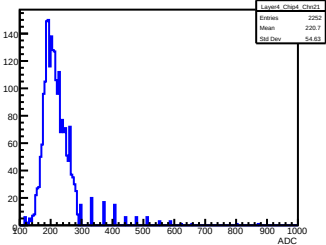
Layer 4, Chip 4, Channel 12



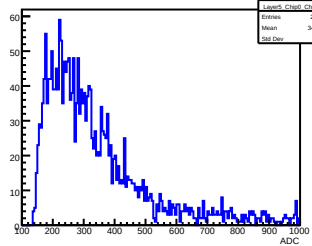
Layer 4, Chip 4, Channel 14



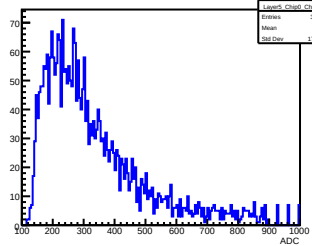
Layer 4, Chip 4, Channel 21



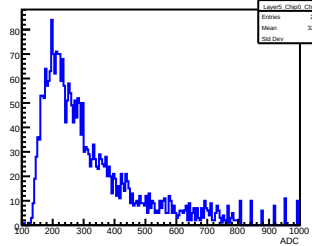
Layer 5, Chip 0, Channel 26



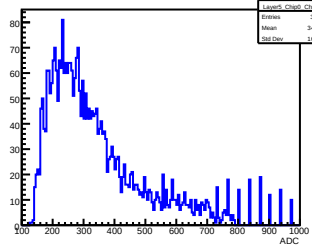
Layer 5, Chip 0, Channel 28



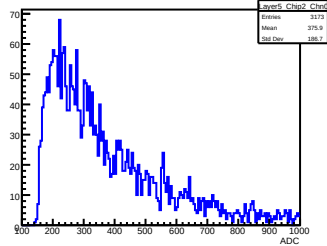
Layer 5, Chip 0, Channel 30



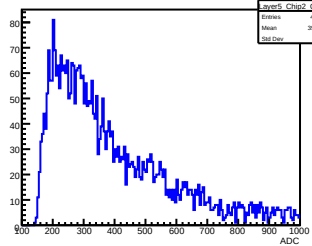
Layer 5, Chip 0, Channel 32



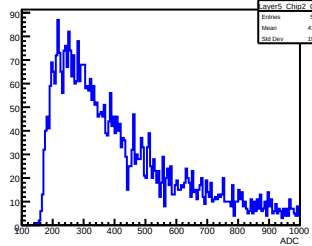
Layer 5, Chip 2, Channel 0



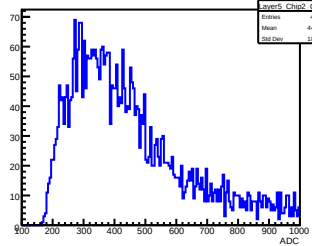
Layer 5, Chip 2, Channel 1



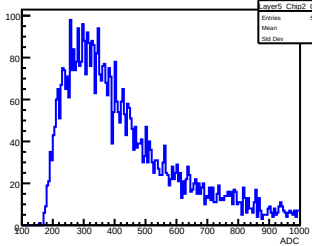
Layer 5, Chip 2, Channel 2



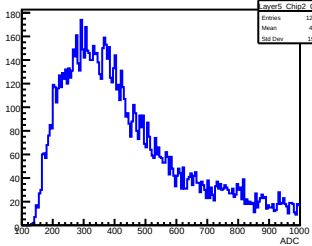
Layer 5, Chip 2, Channel 4



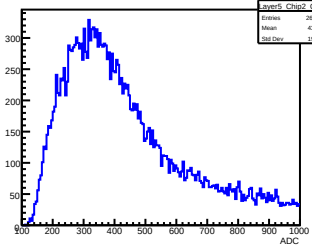
Layer 5, Chip 2, Channel 5



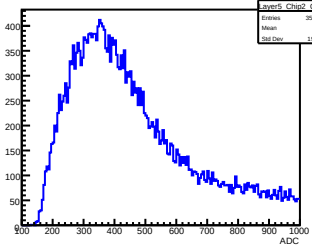
Layer 5, Chip 2, Channel 6



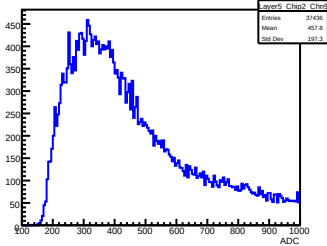
Layer 5, Chip 2, Channel 7



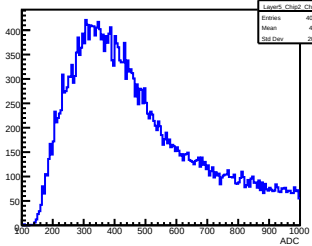
Layer 5, Chip 2, Channel 8



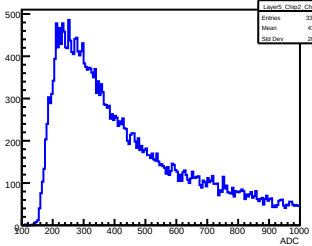
Layer 5, Chip 2, Channel 9



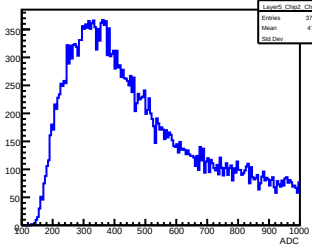
Layer 5, Chip 2, Channel 10



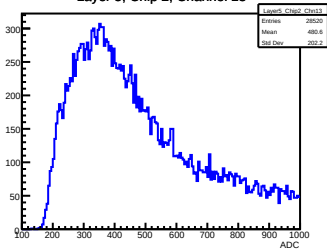
Layer 5, Chip 2, Channel 11



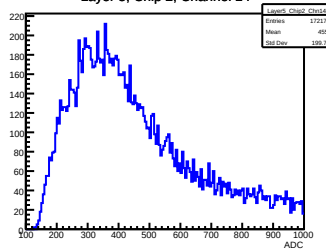
Layer 5, Chip 2, Channel 12



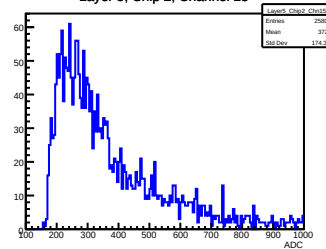
Layer 5, Chip 2, Channel 13



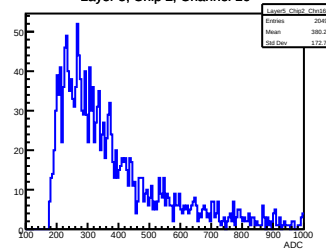
Layer 5, Chip 2, Channel 14



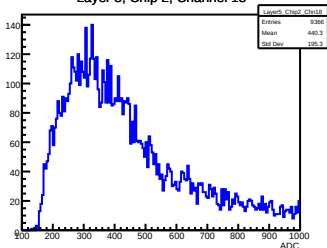
Layer 5, Chip 2, Channel 15



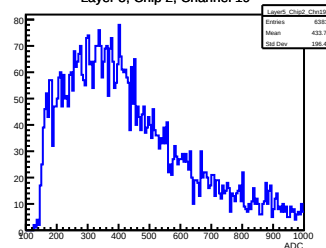
Layer 5, Chip 2, Channel 16



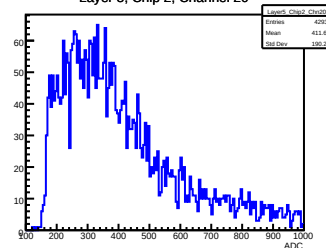
Layer 5, Chip 2, Channel 18



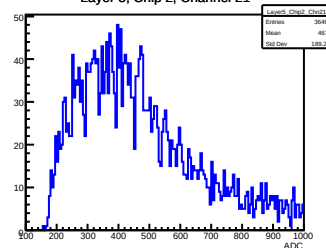
Layer 5, Chip 2, Channel 19



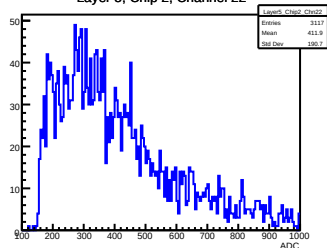
Layer 5, Chip 2, Channel 20



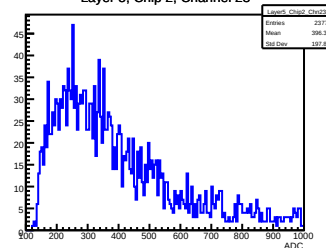
Layer 5, Chip 2, Channel 21



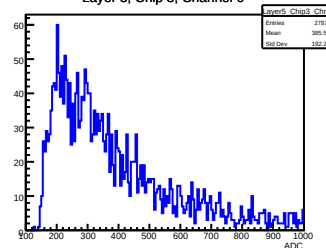
Layer 5, Chip 2, Channel 22



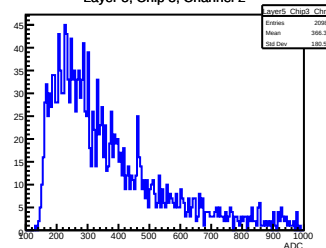
Layer 5, Chip 2, Channel 23



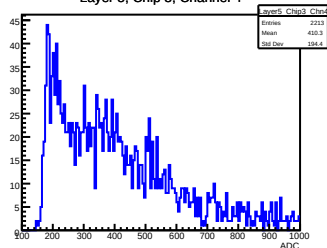
Layer 5, Chip 3, Channel 0



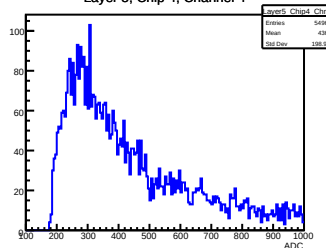
Layer 5, Chip 3, Channel 2



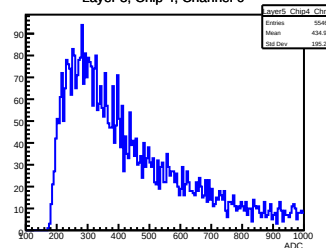
Layer 5, Chip 3, Channel 4



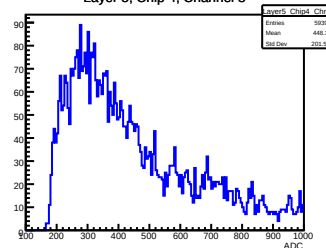
Layer 5, Chip 4, Channel 4



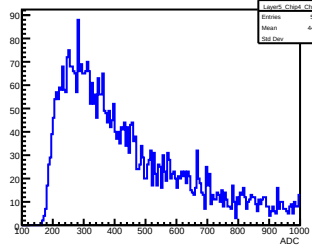
Layer 5, Chip 4, Channel 6



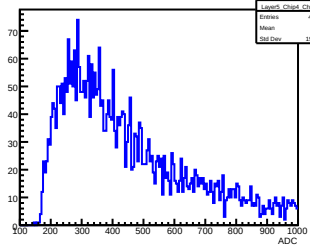
Layer 5, Chip 4, Channel 8



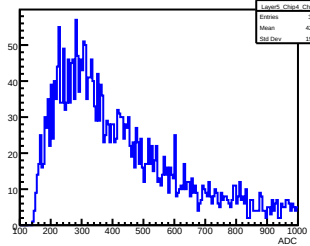
Layer 5, Chip 4, Channel 10



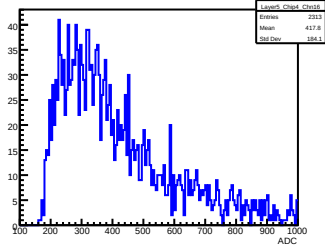
Layer 5, Chip 4, Channel 12



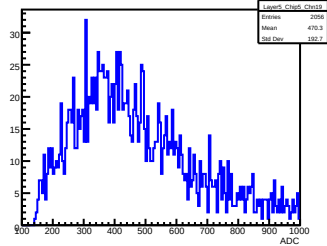
Layer 5, Chip 4, Channel 14



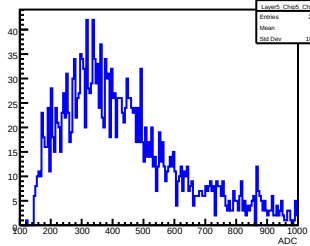
Layer 5, Chip 4, Channel 16



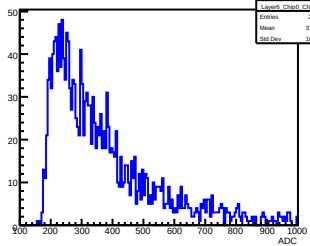
Layer 5, Chip 5, Channel 19



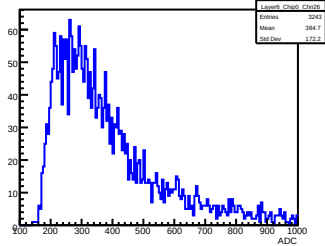
Layer 5, Chip 5, Channel 20



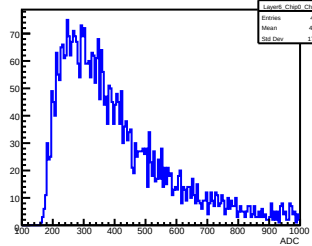
Layer 6, Chip 0, Channel 24



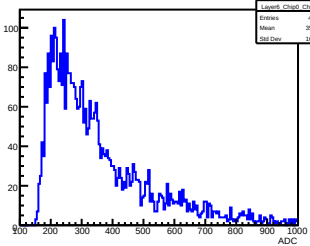
Layer 6, Chip 0, Channel 26



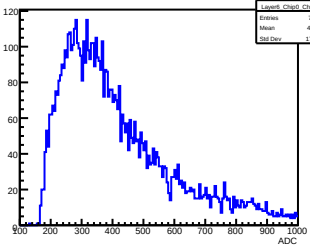
Layer 6, Chip 0, Channel 28



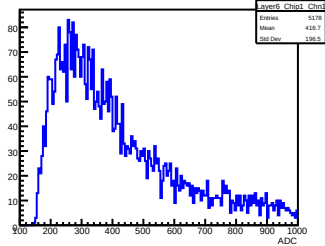
Layer 6, Chip 0, Channel 30



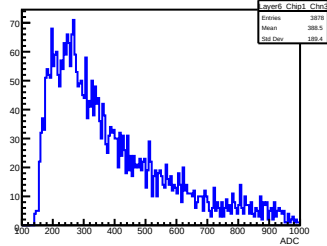
Layer 6, Chip 0, Channel 32



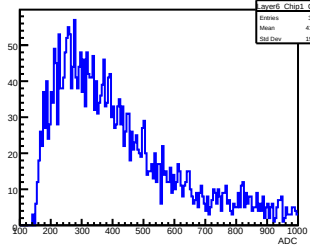
Layer 6, Chip 1, Channel 1



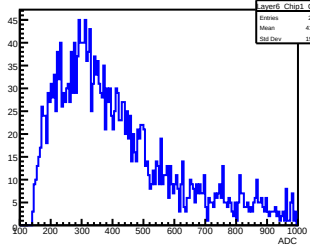
Layer 6, Chip 1, Channel 3



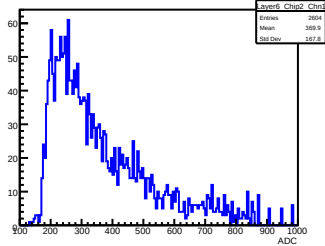
Layer 6, Chip 1, Channel 5



Layer 6, Chip 1, Channel 7

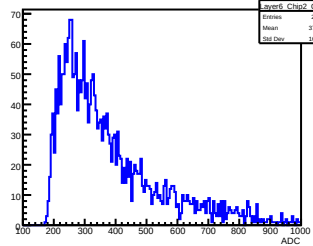


Layer 6, Chip 2, Channel 1



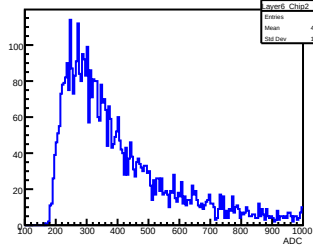
Layer 6, Chip 2, Channel 5

Layer6	Chip2	Chn5
Entries	3163	
Mean	379.6	
Std Dev	164.8	



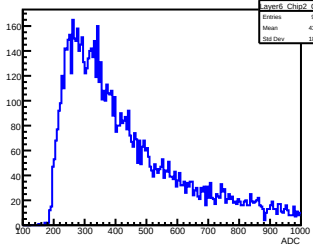
Layer 6, Chip 2, Channel 6

Layer6	Chip2	Chn6
Entries	3100	
Mean	401.5	
Std Dev	178.5	



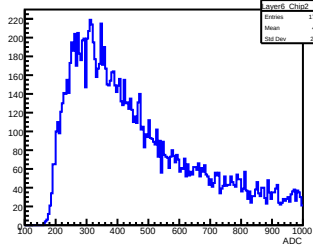
Layer 6, Chip 2, Channel 7

Layer6	Chip2	Chn7
Entries	3799	
Mean	430.7	
Std Dev	186.5	



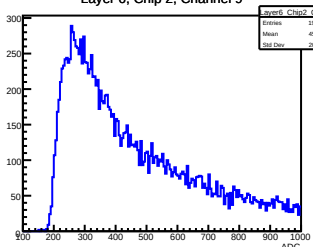
Layer 6, Chip 2, Channel 8

Layer6	Chip2	Chn8
Entries	3719	
Mean	465.1	
Std Dev	202.4	



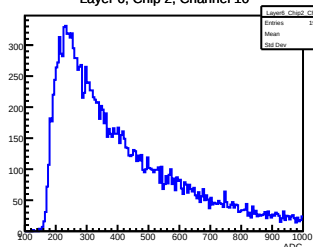
Layer 6, Chip 2, Channel 9

Layer6	Chip2	Chn9
Entries	1893	
Mean	457.4	
Std Dev	205.2	



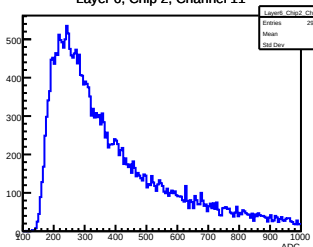
Layer 6, Chip 2, Channel 10

Layer6	Chip2	Chn10
Entries	1894	
Mean	451	
Std Dev	181	



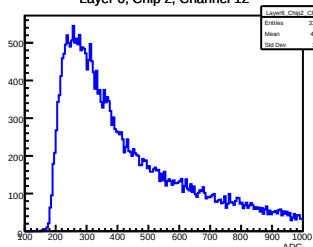
Layer 6, Chip 2, Channel 11

Layer6	Chip2	Chn11
Entries	2052	
Mean	388	
Std Dev	190	



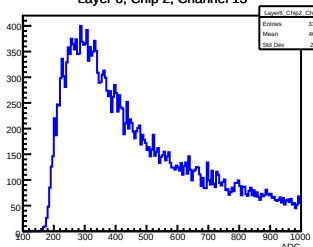
Layer 6, Chip 2, Channel 12

Layer6	Chip2	Chn12
Entries	3118	
Mean	428.1	
Std Dev	195.1	



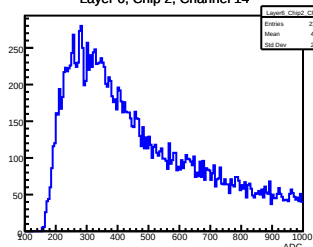
Layer 6, Chip 2, Channel 13

Layer6	Chip2	Chn13
Entries	1881	
Mean	466.7	
Std Dev	208.1	



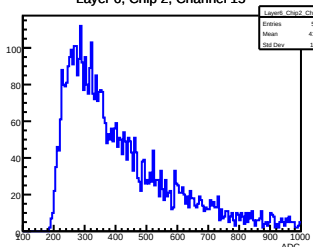
Layer 6, Chip 2, Channel 14

Layer6	Chip2	Chn14
Entries	4339	
Mean	471.3	
Std Dev	212.2	



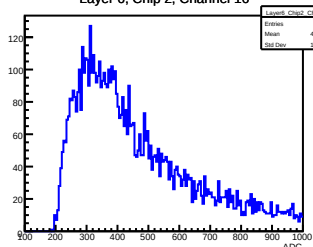
Layer 6, Chip 2, Channel 15

Layer6	Chip2	Chn15
Entries	1411	
Mean	414.8	
Std Dev	173.1	



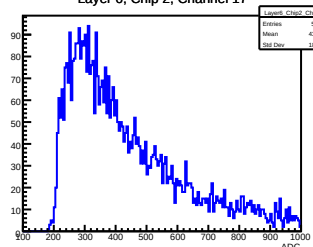
Layer 6, Chip 2, Channel 16

Layer6	Chip2	Chn16
Entries	969	
Mean	460.5	
Std Dev	198.5	



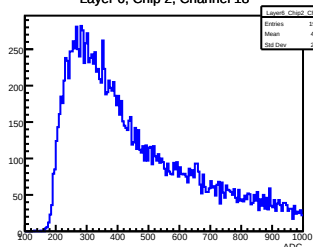
Layer 6, Chip 2, Channel 17

Layer6	Chip2	Chn17
Entries	5536	
Mean	437.7	
Std Dev	183.8	



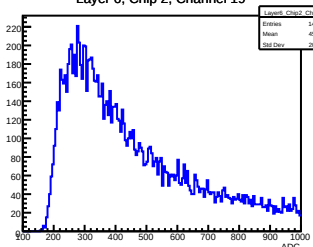
Layer 6, Chip 2, Channel 18

Layer6	Chip2	Chn18
Entries	1959	
Mean	453.4	
Std Dev	202.5	



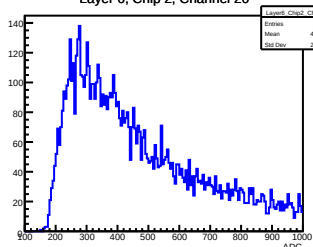
Layer 6, Chip 2, Channel 19

Layer6	Chip2	Chn19
Entries	1489	
Mean	455.9	
Std Dev	202.5	

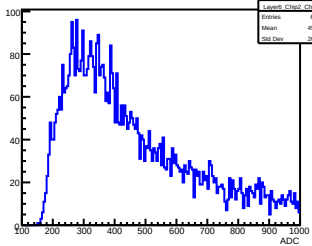


Layer 6, Chip 2, Channel 20

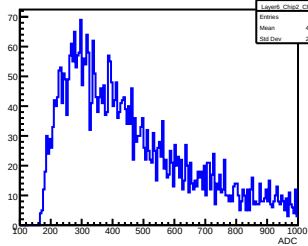
Layer6	Chip2	Chn20
Entries	8951	
Mean	444.7	
Std Dev	204.2	



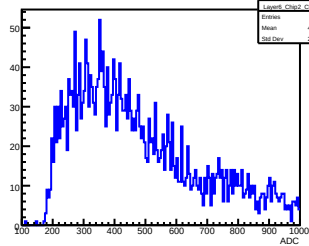
Layer 6, Chip 2, Channel 21



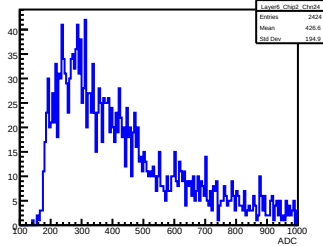
Layer 6, Chip 2, Channel 22



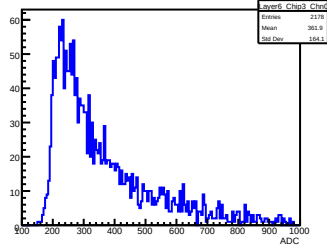
Layer 6, Chip 2, Channel 23



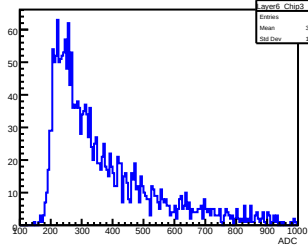
Layer 6, Chip 2, Channel 24



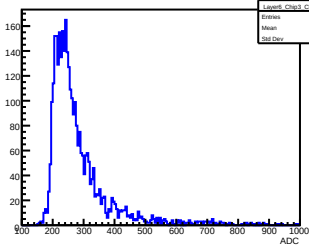
Layer 6, Chip 3, Channel 0



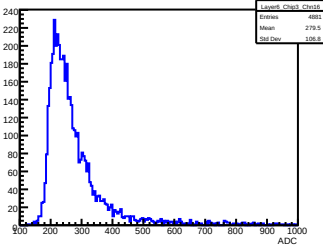
Layer 6, Chip 3, Channel 2



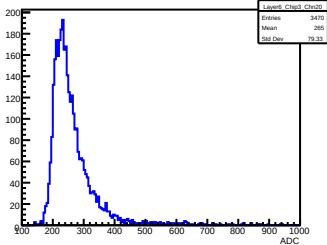
Layer 6, Chip 3, Channel 14



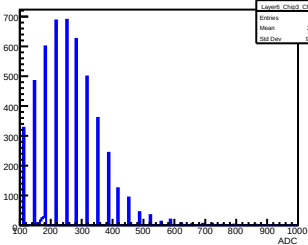
Layer 6, Chip 3, Channel 16



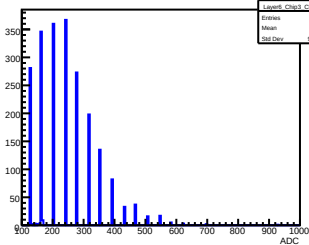
Layer 6, Chip 3, Channel 20



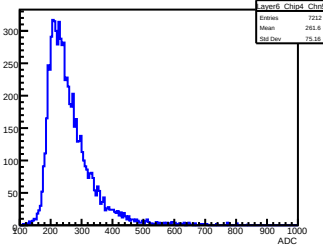
Layer 6, Chip 3, Channel 27



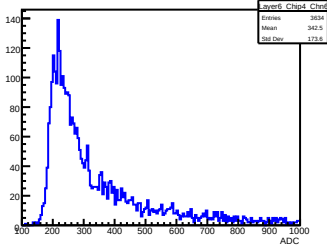
Layer 6, Chip 3, Channel 29



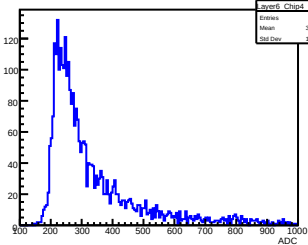
Layer 6, Chip 4, Channel 5



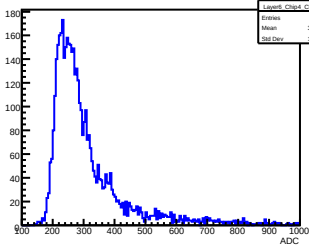
Layer 6, Chip 4, Channel 6



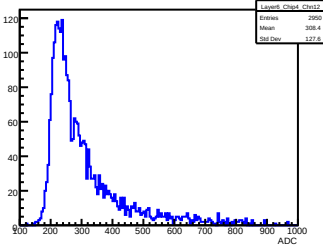
Layer 6, Chip 4, Channel 8



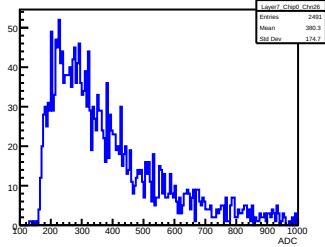
Layer 6, Chip 4, Channel 10



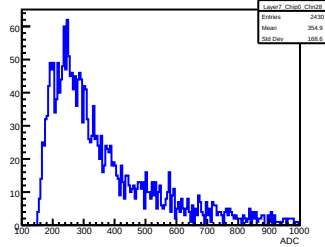
Layer 6, Chip 4, Channel 12



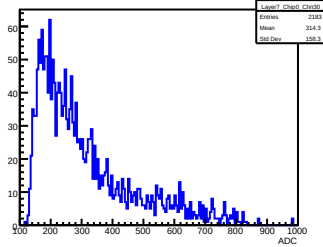
Layer 7, Chip 0, Channel 26



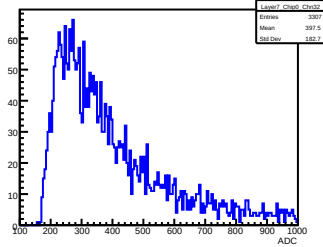
Layer 7, Chip 0, Channel 28



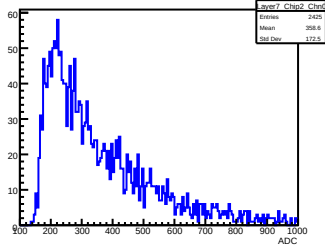
Layer 7, Chip 0, Channel 30



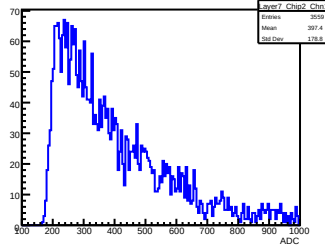
Layer 7, Chip 0, Channel 32



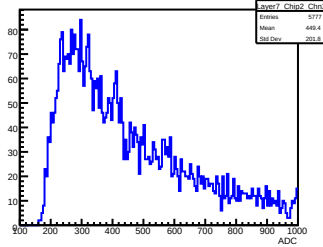
Layer 7, Chip 2, Channel 0



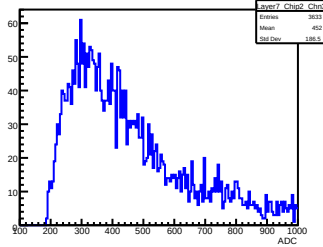
Layer 7, Chip 2, Channel 1



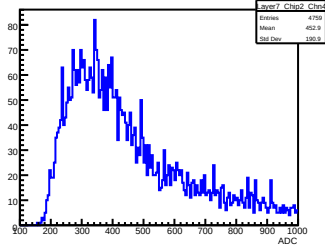
Layer 7, Chip 2, Channel 2



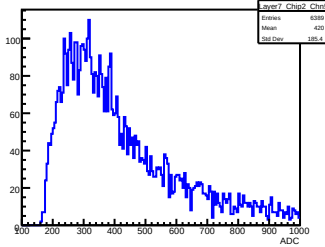
Layer 7, Chip 2, Channel 3



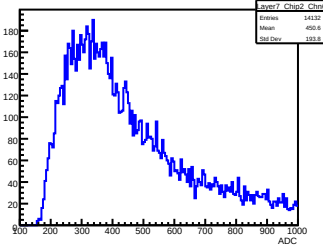
Layer 7, Chip 2, Channel 4



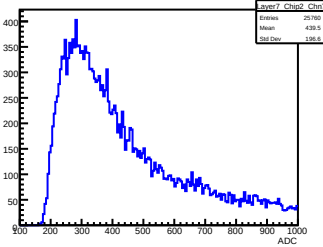
Layer 7, Chip 2, Channel 5



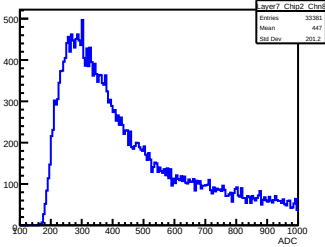
Layer 7, Chip 2, Channel 6



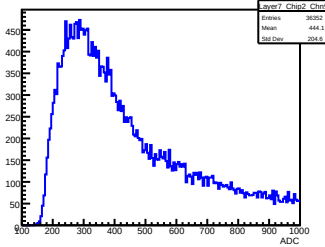
Layer 7, Chip 2, Channel 7



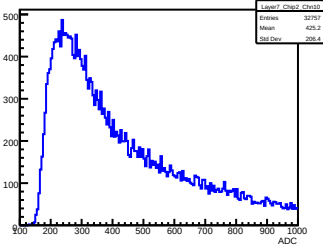
Layer 7, Chip 2, Channel 8



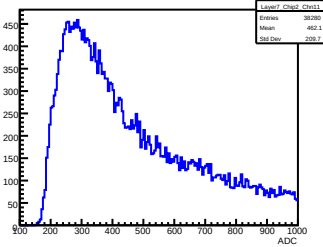
Layer 7, Chip 2, Channel 9



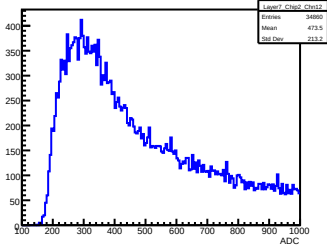
Layer 7, Chip 2, Channel 10



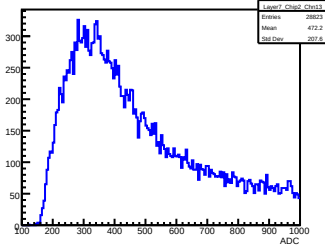
Layer 7, Chip 2, Channel 11



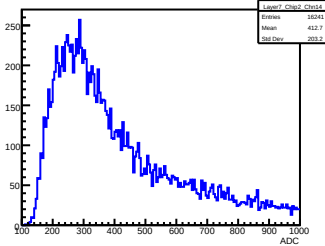
Layer 7, Chip 2, Channel 12



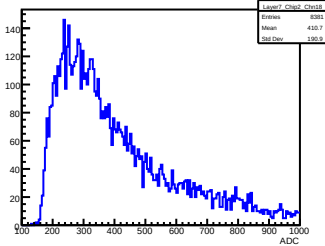
Layer 7, Chip 2, Channel 13



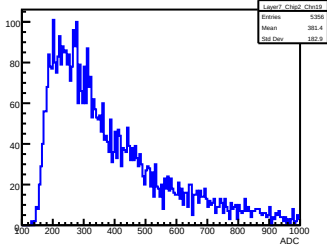
Layer 7, Chip 2, Channel 14



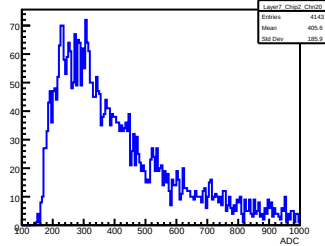
Layer 7, Chip 2, Channel 18



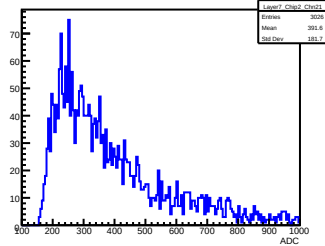
Layer 7, Chip 2, Channel 19



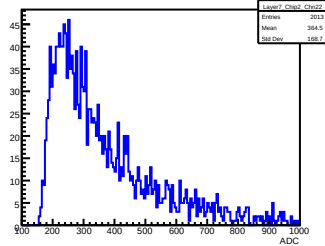
Layer 7, Chip 2, Channel 20



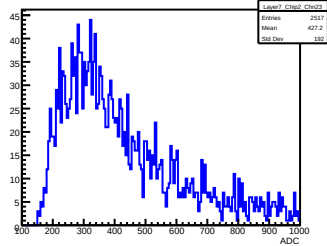
Layer 7, Chip 2, Channel 21



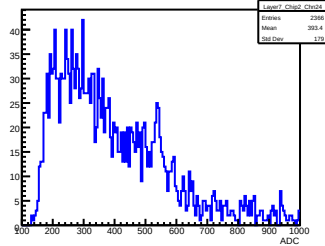
Layer 7, Chip 2, Channel 22



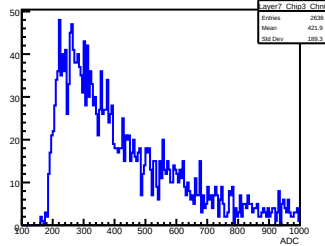
Layer 7, Chip 2, Channel 23



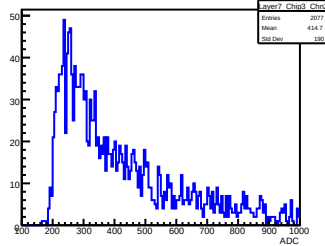
Layer 7, Chip 2, Channel 24



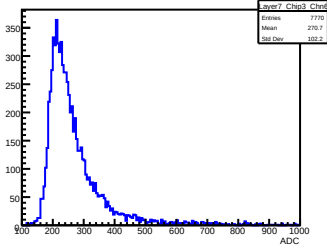
Layer 7, Chip 3, Channel 0



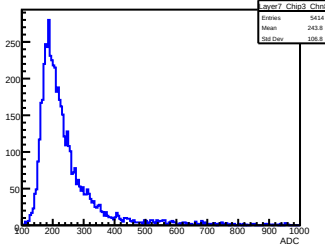
Layer 7, Chip 3, Channel 2



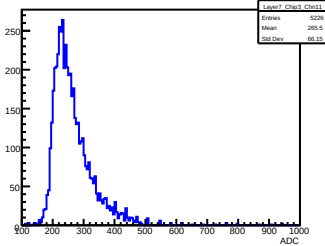
Layer 7, Chip 3, Channel 6



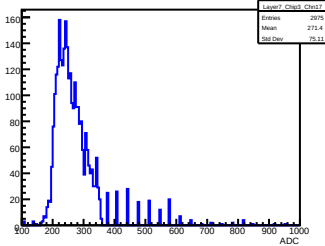
Layer 7, Chip 3, Channel 8



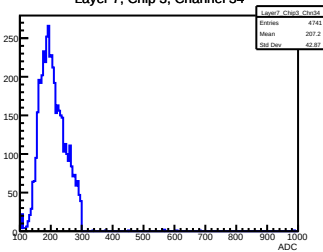
Layer 7, Chip 3, Channel 11



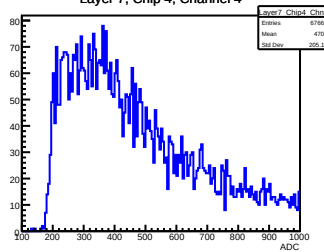
Layer 7, Chip 3, Channel 17



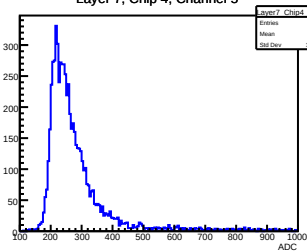
Layer 7, Chip 3, Channel 34



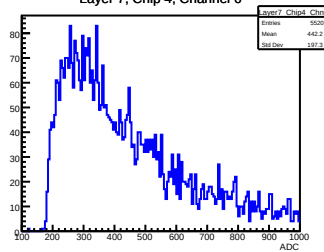
Layer 7, Chip 4, Channel 4



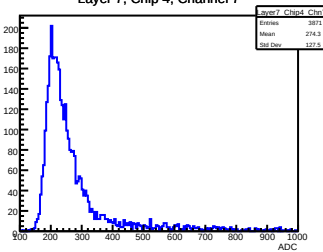
Layer 7, Chip 4, Channel 5



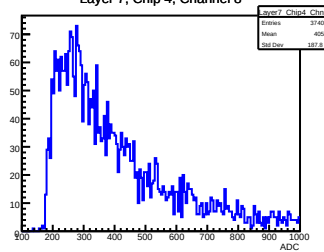
Layer 7, Chip 4, Channel 6



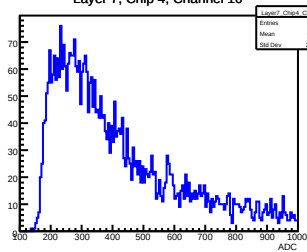
Layer 7, Chip 4, Channel 7



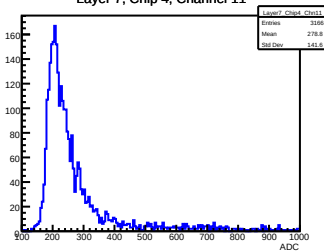
Layer 7, Chip 4, Channel 8



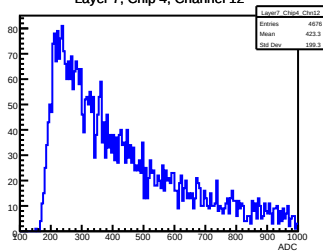
Layer 7, Chip 4, Channel 10



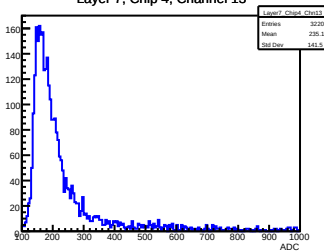
Layer 7, Chip 4, Channel 11



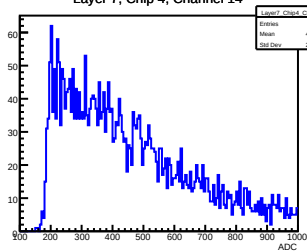
Layer 7, Chip 4, Channel 12



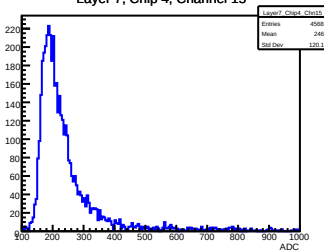
Layer 7, Chip 4, Channel 13



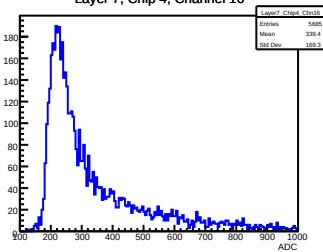
Layer 7, Chip 4, Channel 14



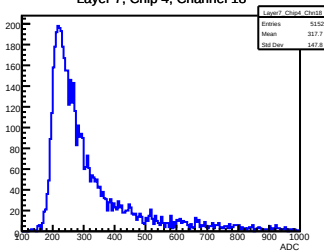
Layer 7, Chip 4, Channel 15



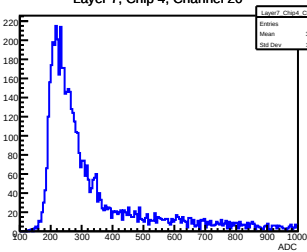
Layer 7, Chip 4, Channel 16



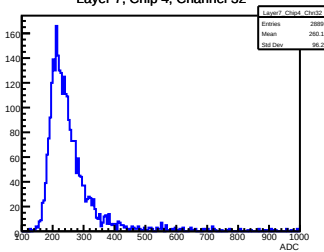
Layer 7, Chip 4, Channel 18



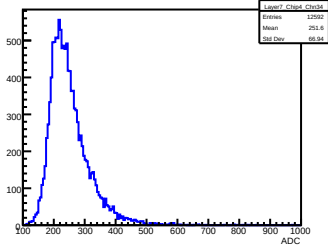
Layer 7, Chip 4, Channel 20



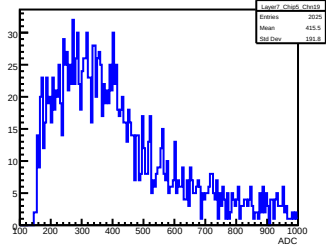
Layer 7, Chip 4, Channel 32



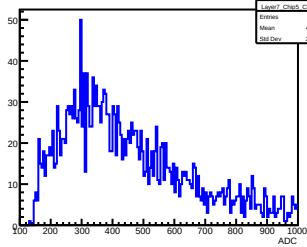
Layer 7, Chip 4, Channel 34



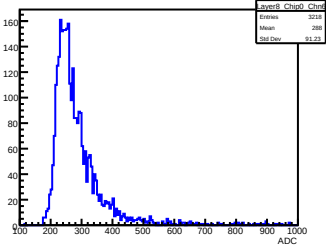
Layer 7, Chip 5, Channel 19



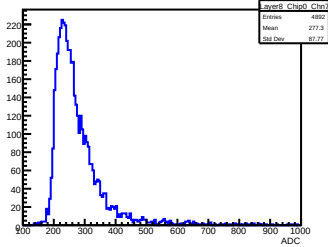
Layer 7, Chip 5, Channel 20



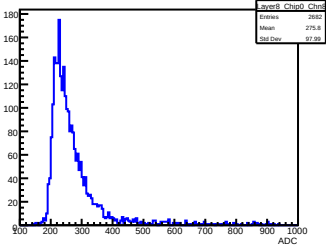
Layer 8, Chip 0, Channel 6



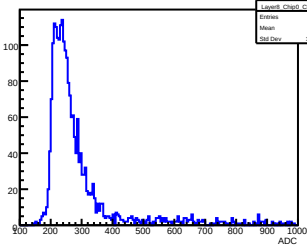
Layer 8, Chip 0, Channel 7



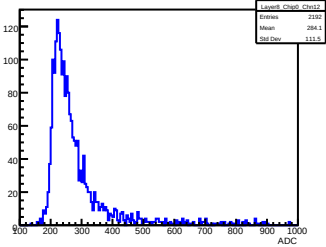
Layer 8, Chip 0, Channel 8



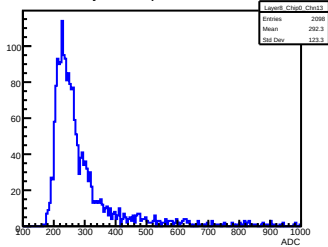
Layer 8, Chip 0, Channel 11



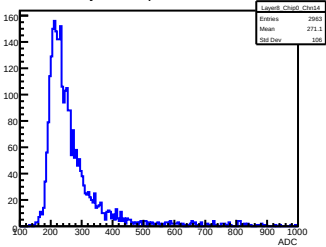
Layer 8, Chip 0, Channel 12



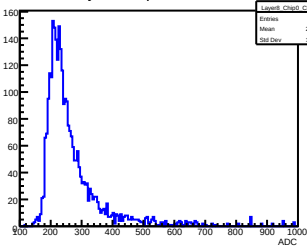
Layer 8, Chip 0, Channel 13



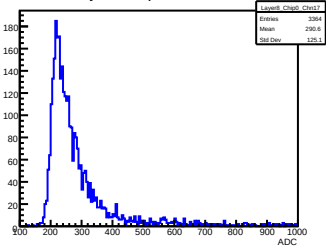
Layer 8, Chip 0, Channel 14



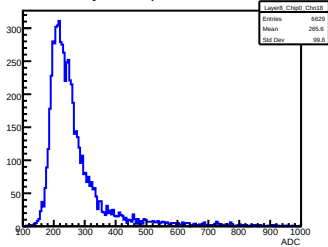
Layer 8, Chip 0, Channel 16



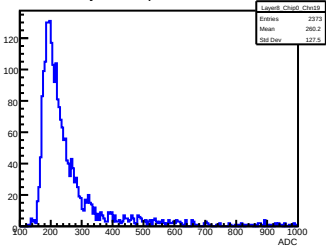
Layer 8, Chip 0, Channel 17



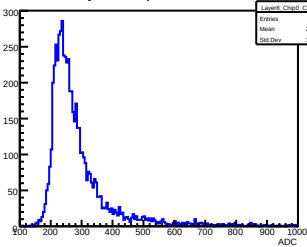
Layer 8, Chip 0, Channel 18



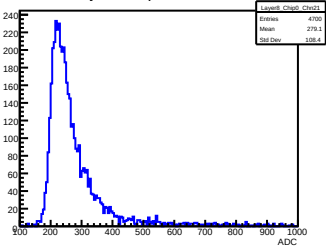
Layer 8, Chip 0, Channel 19



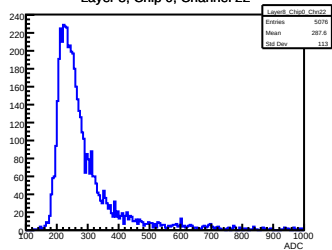
Layer 8, Chip 0, Channel 20



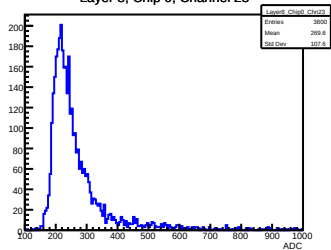
Layer 8, Chip 0, Channel 21



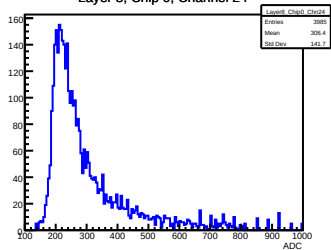
Layer 8, Chip 0, Channel 22



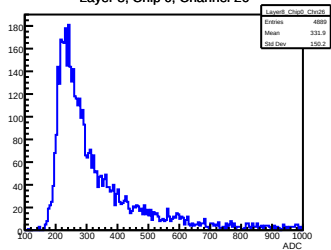
Layer 8, Chip 0, Channel 23



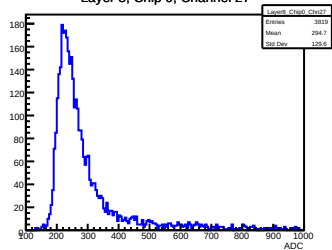
Layer 8, Chip 0, Channel 24



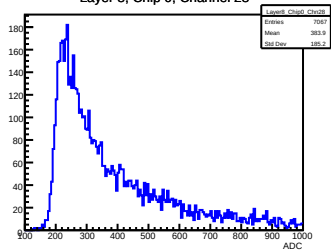
Layer 8, Chip 0, Channel 26



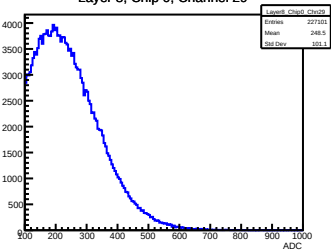
Layer 8, Chip 0, Channel 27



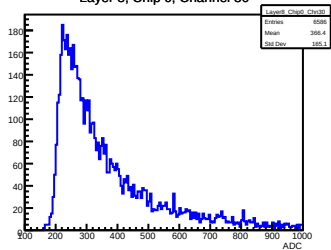
Layer 8, Chip 0, Channel 28



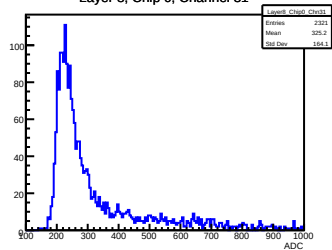
Layer 8, Chip 0, Channel 29



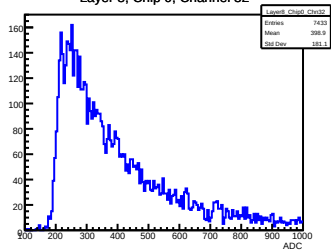
Layer 8, Chip 0, Channel 30



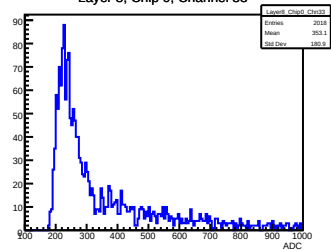
Layer 8, Chip 0, Channel 31



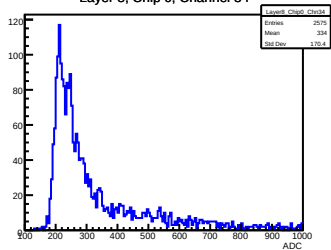
Layer 8, Chip 0, Channel 32



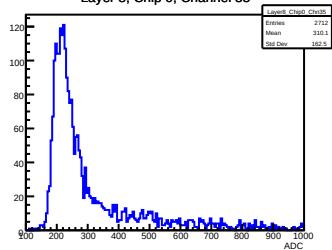
Layer 8, Chip 0, Channel 33



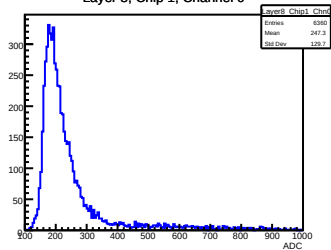
Layer 8, Chip 0, Channel 34



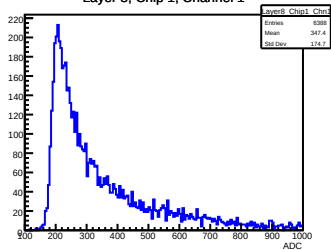
Layer 8, Chip 0, Channel 35



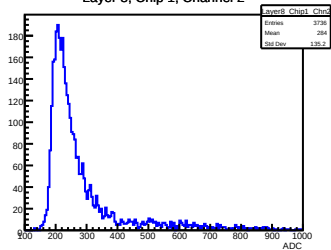
Layer 8, Chip 1, Channel 0



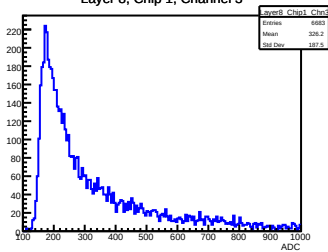
Layer 8, Chip 1, Channel 1



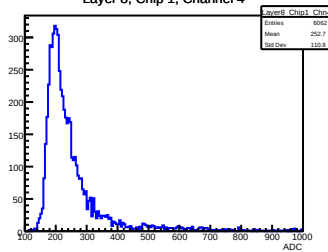
Layer 8, Chip 1, Channel 2



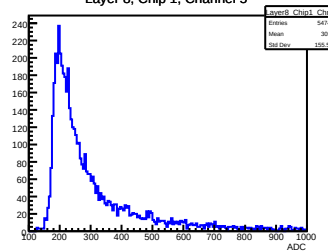
Layer 8, Chip 1, Channel 3



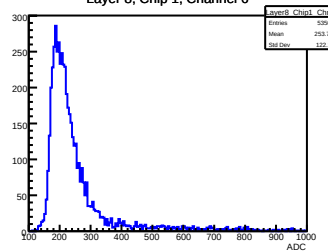
Layer 8, Chip 1, Channel 4



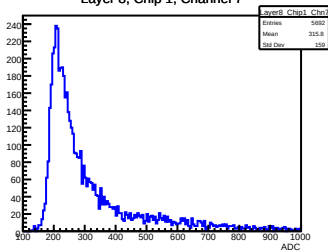
Layer 8, Chip 1, Channel 5



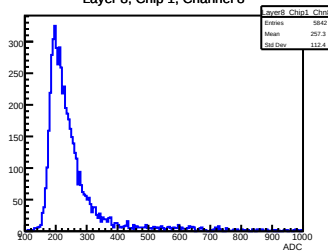
Layer 8, Chip 1, Channel 6



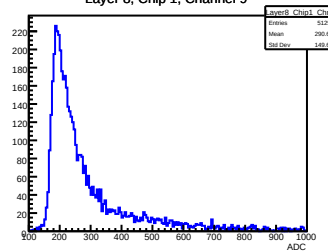
Layer 8, Chip 1, Channel 7



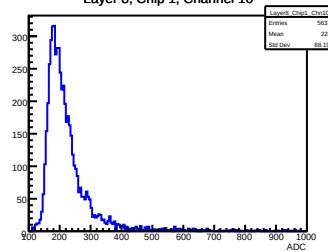
Layer 8, Chip 1, Channel 8



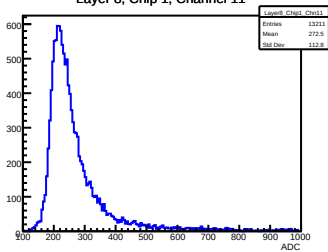
Layer 8, Chip 1, Channel 9



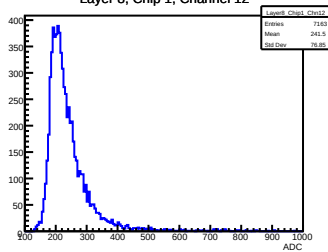
Layer 8, Chip 1, Channel 10



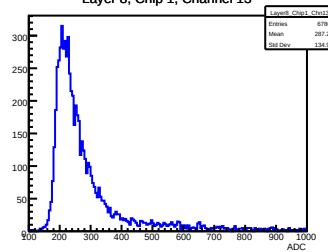
Layer 8, Chip 1, Channel 11



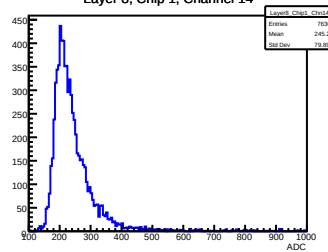
Layer 8, Chip 1, Channel 12



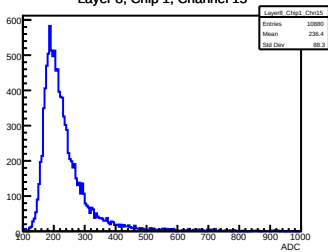
Layer 8, Chip 1, Channel 13



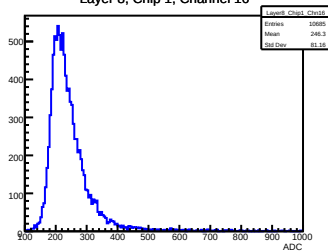
Layer 8, Chip 1, Channel 14



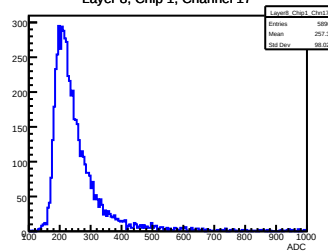
Layer 8, Chip 1, Channel 15



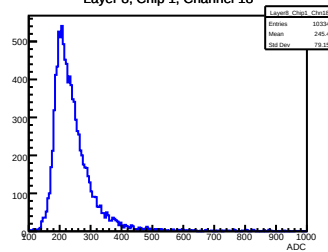
Layer 8, Chip 1, Channel 16



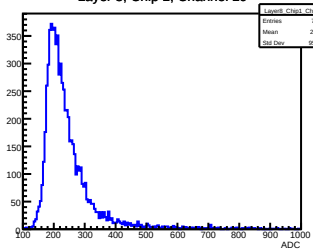
Layer 8, Chip 1, Channel 17



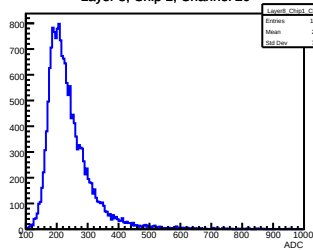
Layer 8, Chip 1, Channel 18



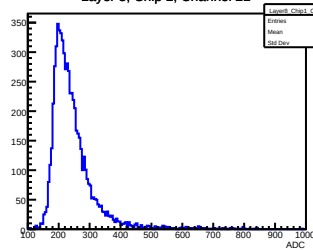
Layer 8, Chip 1, Channel 19



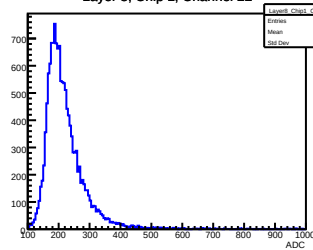
Layer 8, Chip 1, Channel 20



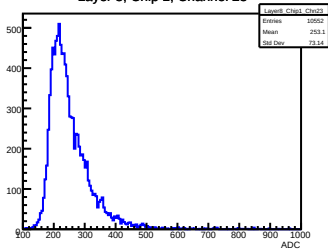
Layer 8, Chip 1, Channel 21



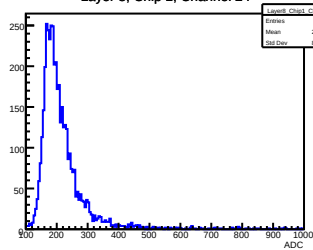
Layer 8, Chip 1, Channel 22



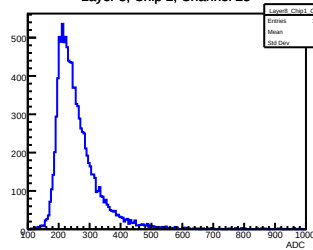
Layer 8, Chip 1, Channel 23



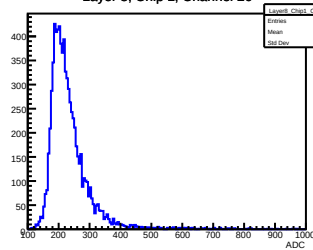
Layer 8, Chip 1, Channel 24



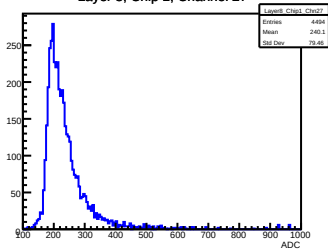
Layer 8, Chip 1, Channel 25



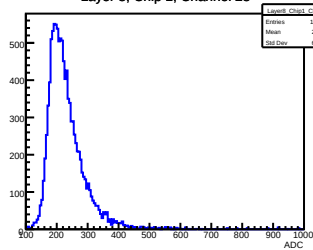
Layer 8, Chip 1, Channel 26



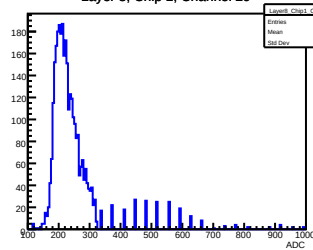
Layer 8, Chip 1, Channel 27



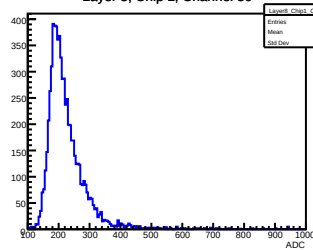
Layer 8, Chip 1, Channel 28



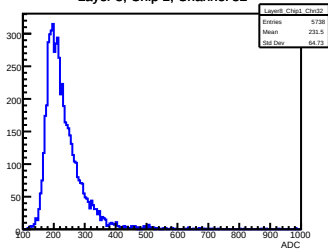
Layer 8, Chip 1, Channel 29



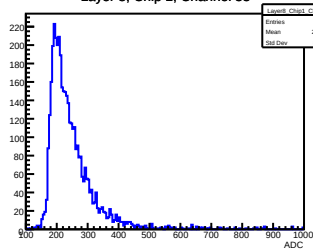
Layer 8, Chip 1, Channel 30



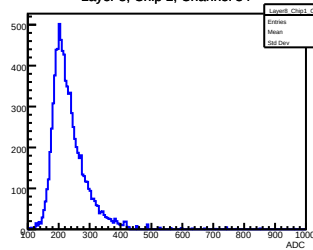
Layer 8, Chip 1, Channel 32



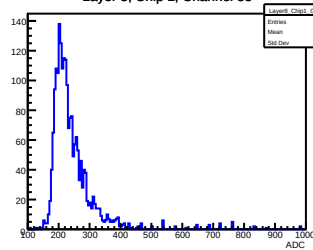
Layer 8, Chip 1, Channel 33



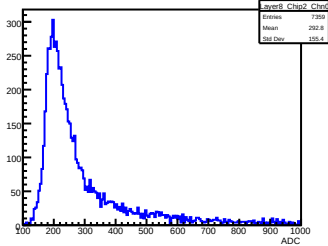
Layer 8, Chip 1, Channel 34



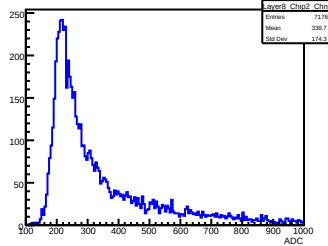
Layer 8, Chip 1, Channel 35



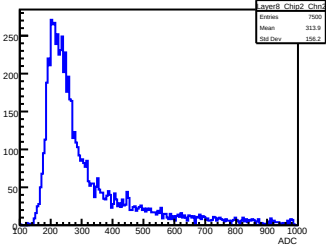
Layer 8, Chip 2, Channel 0



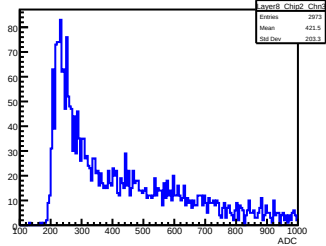
Layer 8, Chip 2, Channel 1



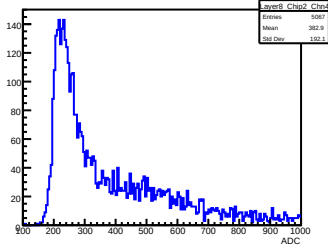
Layer 8, Chip 2, Channel 2



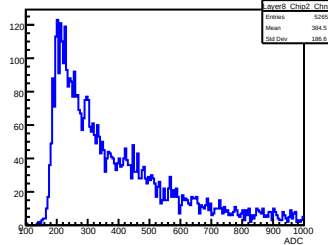
Layer 8, Chip 2, Channel 3



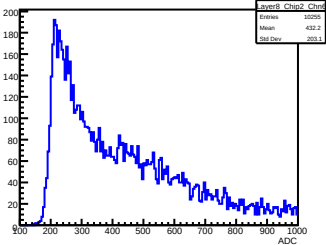
Layer 8, Chip 2, Channel 4



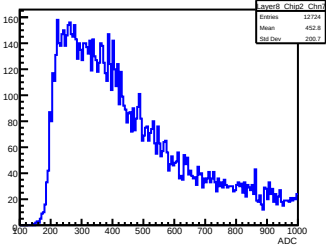
Layer 8, Chip 2, Channel 5



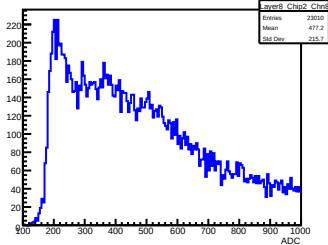
Layer 8, Chip 2, Channel 6



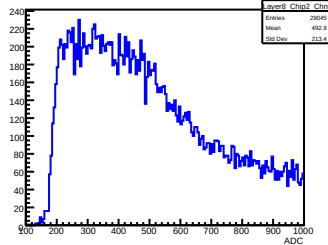
Layer 8, Chip 2, Channel 7



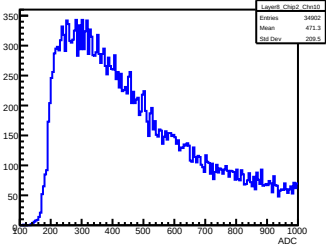
Layer 8, Chip 2, Channel 8



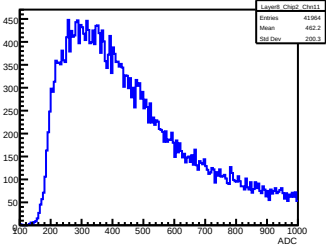
Layer 8, Chip 2, Channel 9



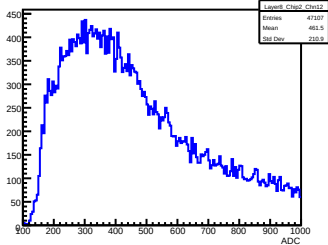
Layer 8, Chip 2, Channel 10



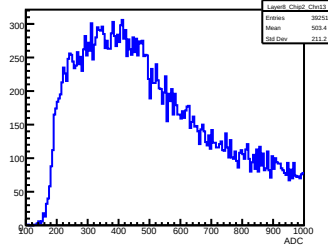
Layer 8, Chip 2, Channel 11



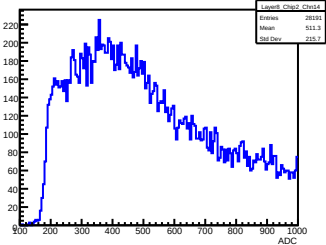
Layer 8, Chip 2, Channel 12



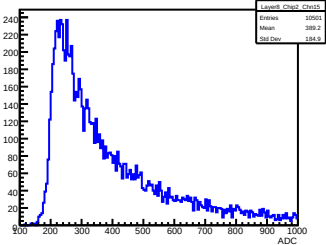
Layer 8, Chip 2, Channel 13



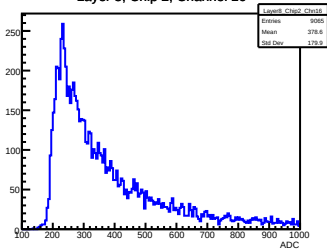
Layer 8, Chip 2, Channel 14



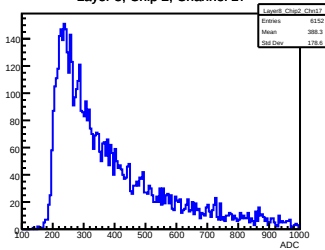
Layer 8, Chip 2, Channel 15



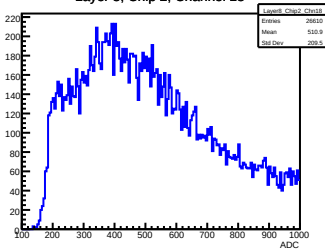
Layer 8, Chip 2, Channel 16



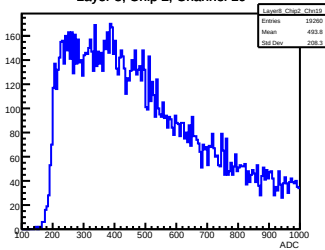
Layer 8, Chip 2, Channel 17



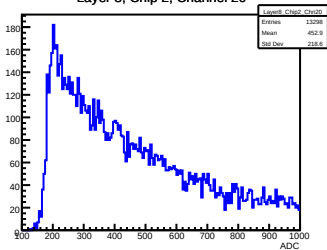
Layer 8, Chip 2, Channel 18



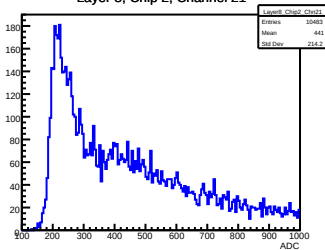
Layer 8, Chip 2, Channel 19



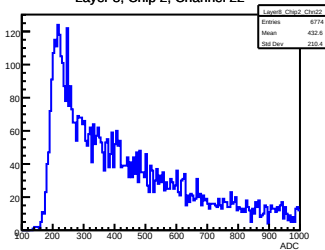
Layer 8, Chip 2, Channel 20



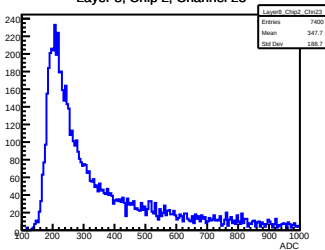
Layer 8, Chip 2, Channel 21



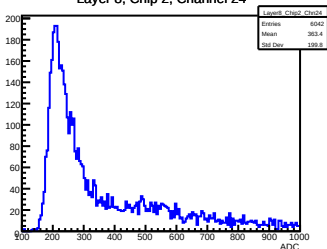
Layer 8, Chip 2, Channel 22



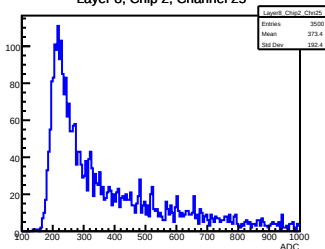
Layer 8, Chip 2, Channel 23



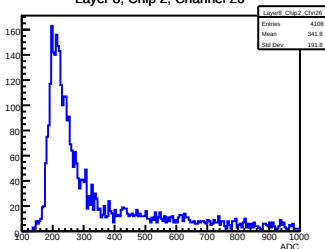
Layer 8, Chip 2, Channel 24



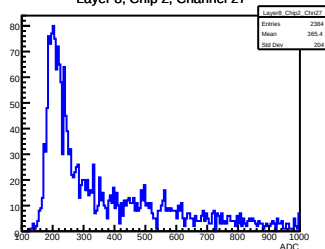
Layer 8, Chip 2, Channel 25



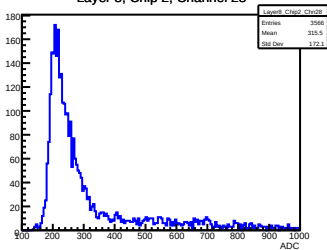
Layer 8, Chip 2, Channel 26



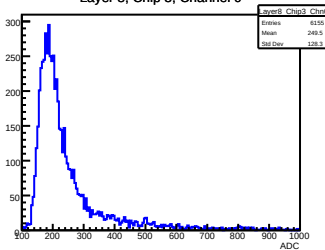
Layer 8, Chip 2, Channel 27



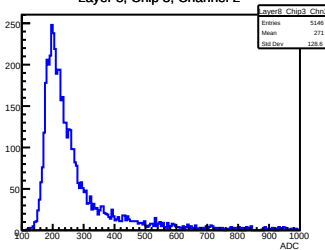
Layer 8, Chip 2, Channel 28



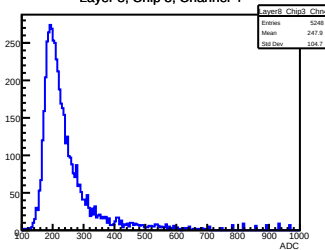
Layer 8, Chip 3, Channel 0



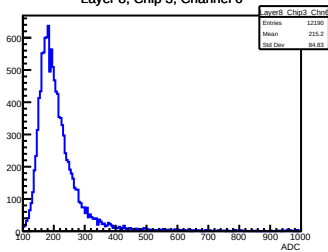
Layer 8, Chip 3, Channel 2



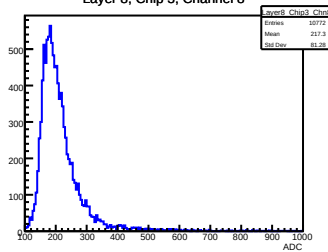
Layer 8, Chip 3, Channel 4



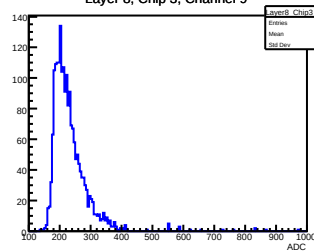
Layer 8, Chip 3, Channel 6



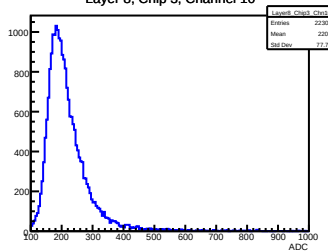
Layer 8, Chip 3, Channel 8



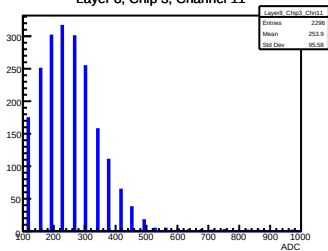
Layer 8, Chip 3, Channel 9



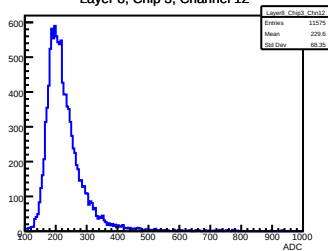
Layer 8, Chip 3, Channel 10



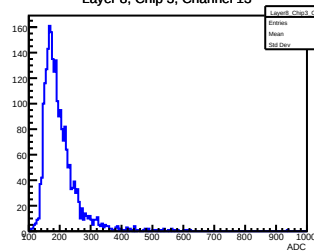
Layer 8, Chip 3, Channel 11



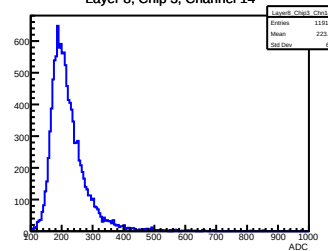
Layer 8, Chip 3, Channel 12



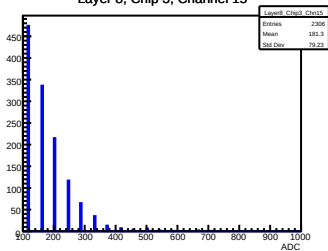
Layer 8, Chip 3, Channel 13



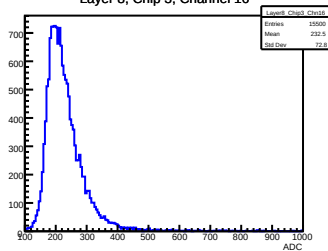
Layer 8, Chip 3, Channel 14



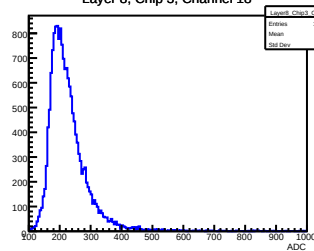
Layer 8, Chip 3, Channel 15



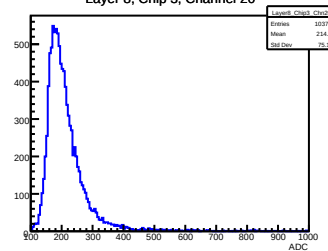
Layer 8, Chip 3, Channel 16



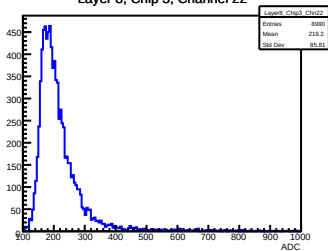
Layer 8, Chip 3, Channel 18



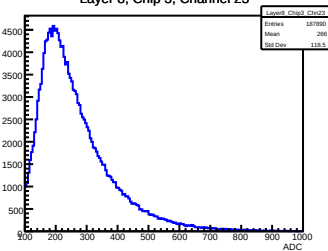
Layer 8, Chip 3, Channel 20



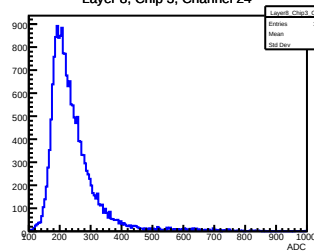
Layer 8, Chip 3, Channel 22



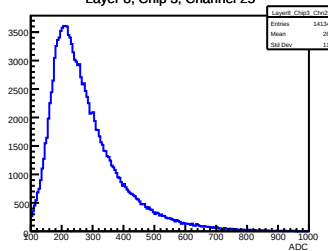
Layer 8, Chip 3, Channel 23



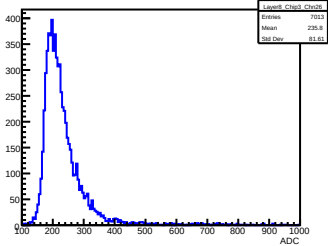
Layer 8, Chip 3, Channel 24



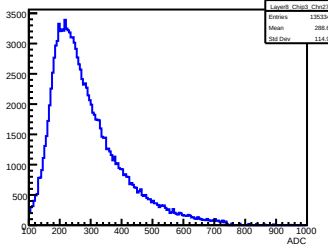
Layer 8, Chip 3, Channel 25



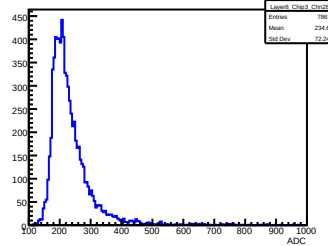
Layer 8, Chip 3, Channel 26



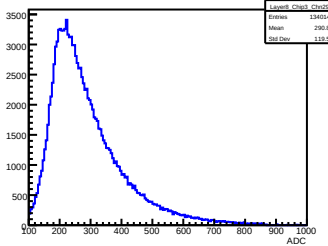
Layer 8, Chip 3, Channel 27



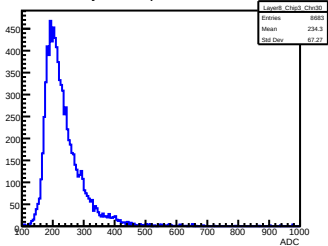
Layer 8, Chip 3, Channel 28



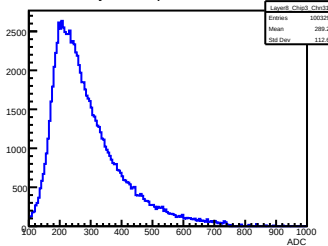
Layer 8, Chip 3, Channel 29



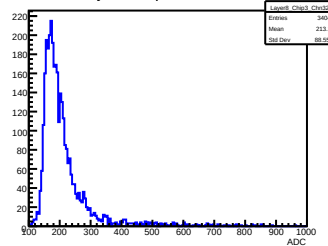
Layer 8, Chip 3, Channel 30



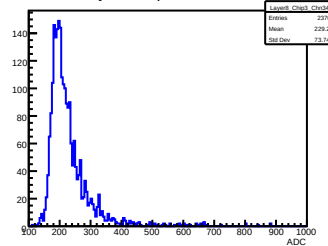
Layer 8, Chip 3, Channel 31



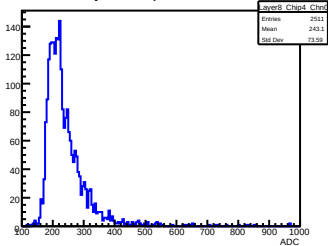
Layer 8, Chip 3, Channel 32



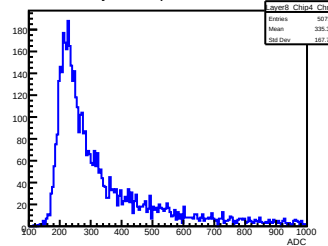
Layer 8, Chip 3, Channel 34



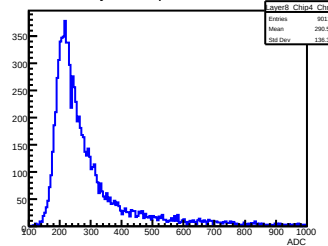
Layer 8, Chip 4, Channel 0



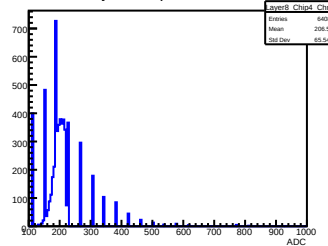
Layer 8, Chip 4, Channel 4



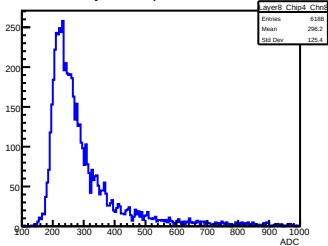
Layer 8, Chip 4, Channel 6



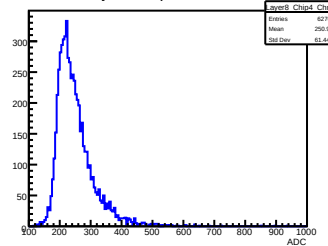
Layer 8, Chip 4, Channel 7



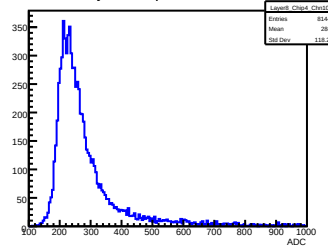
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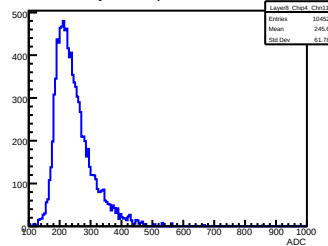
Layer 8, Chip 4, Channel 9



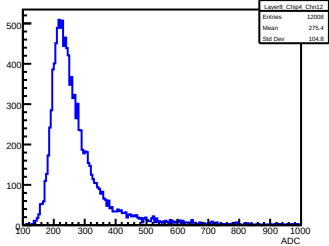
Layer 8, Chip 4, Channel 10



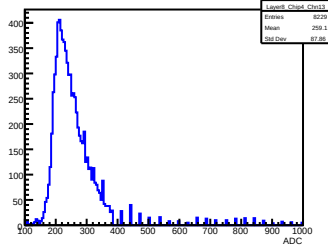
Layer 8, Chip 4, Channel 11



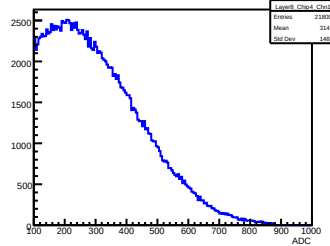
Layer 8, Chip 4, Channel 12



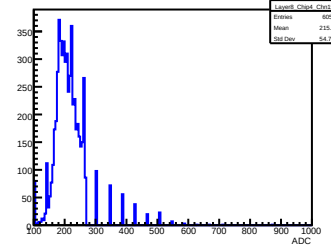
Layer 8, Chip 4, Channel 13



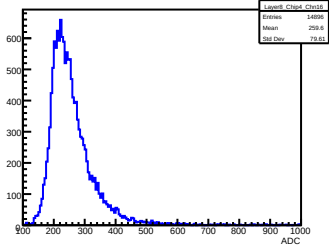
Layer 8, Chip 4, Channel 14



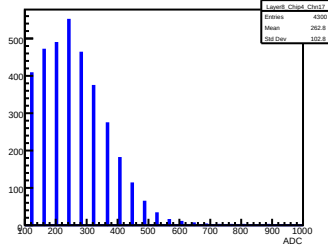
Layer 8, Chip 4, Channel 15



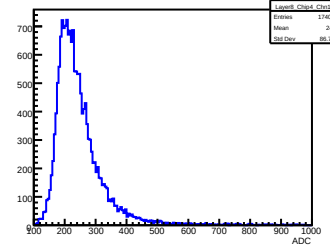
Layer 8, Chip 4, Channel 16



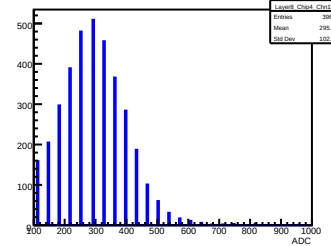
Layer 8, Chip 4, Channel 17



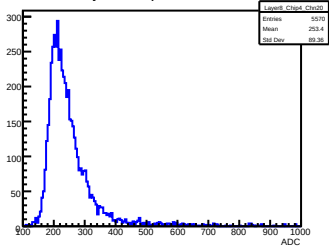
Layer 8, Chip 4, Channel 18



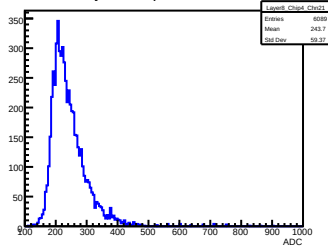
Layer 8, Chip 4, Channel 19



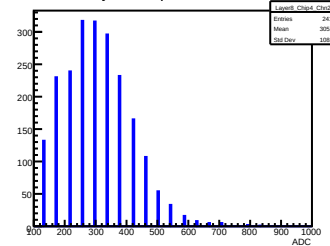
Layer 8, Chip 4, Channel 20



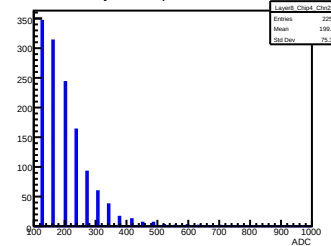
Layer 8, Chip 4, Channel 21



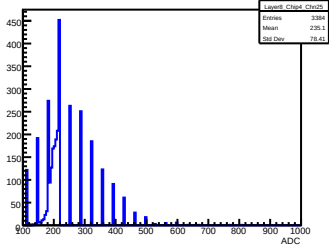
Layer 8, Chip 4, Channel 22



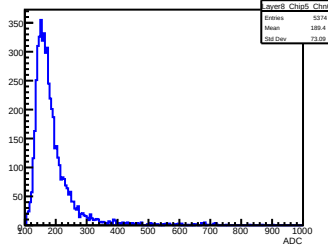
Layer 8, Chip 4, Channel 23



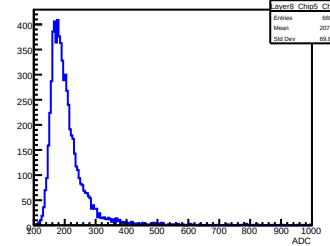
Layer 8, Chip 4, Channel 25



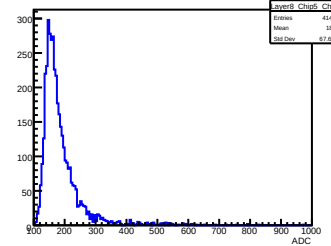
Layer 8, Chip 5, Channel 0



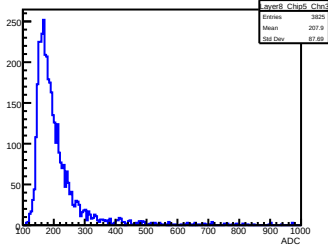
Layer 8, Chip 5, Channel 1



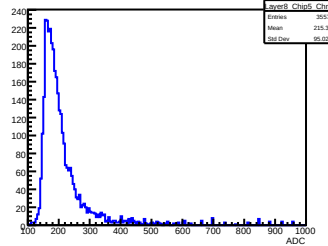
Layer 8, Chip 5, Channel 2



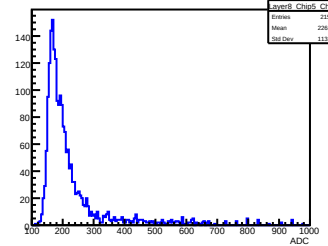
Layer 8, Chip 5, Channel 3



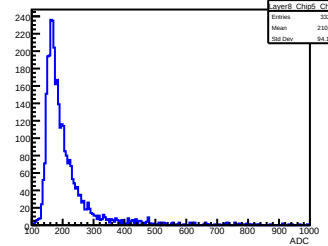
Layer 8, Chip 5, Channel 4



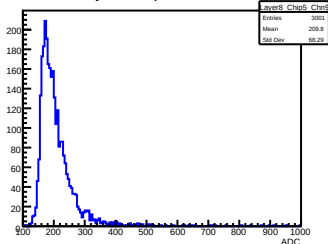
Layer 8, Chip 5, Channel 5



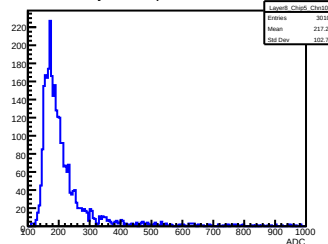
Layer 8, Chip 5, Channel 6



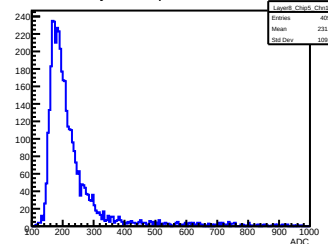
Layer 8, Chip 5, Channel 9



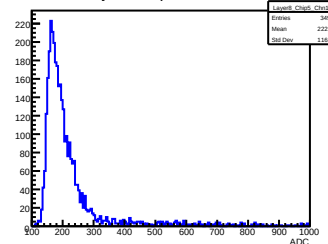
Layer 8, Chip 5, Channel 10



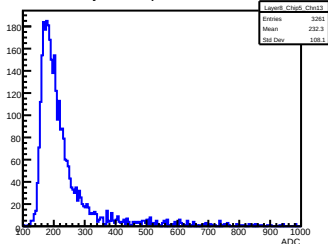
Layer 8, Chip 5, Channel 11



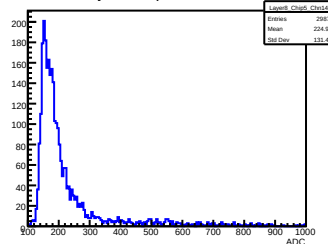
Layer 8, Chip 5, Channel 12



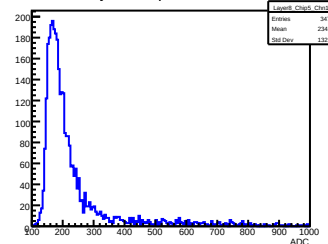
Layer 8, Chip 5, Channel 13



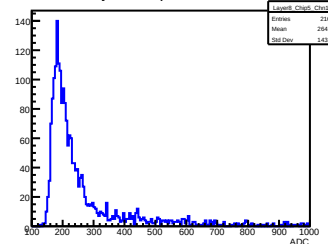
Layer 8, Chip 5, Channel 14



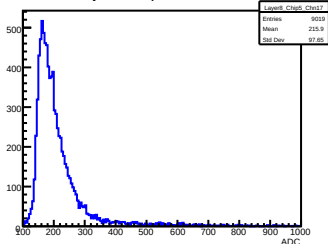
Layer 8, Chip 5, Channel 15



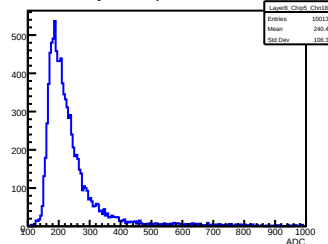
Layer 8, Chip 5, Channel 16



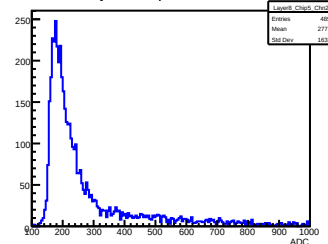
Layer 8, Chip 5, Channel 17



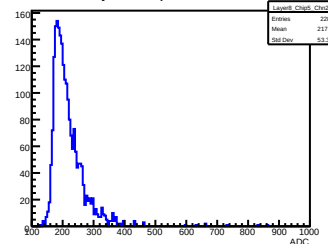
Layer 8, Chip 5, Channel 18



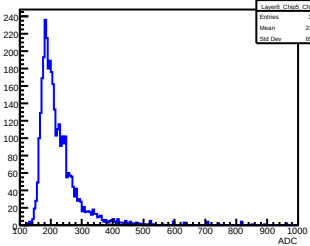
Layer 8, Chip 5, Channel 20



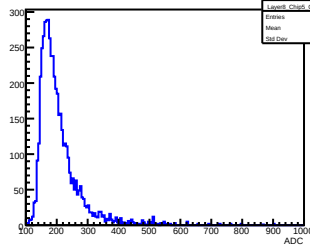
Layer 8, Chip 5, Channel 21



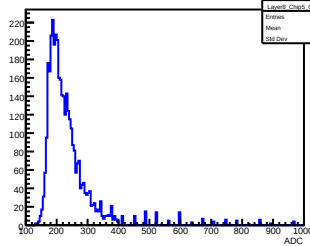
Layer 8, Chip 5, Channel 23



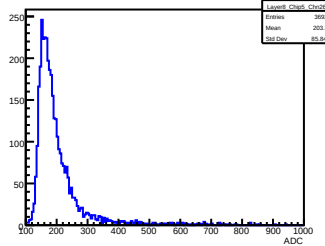
Layer 8, Chip 5, Channel 24



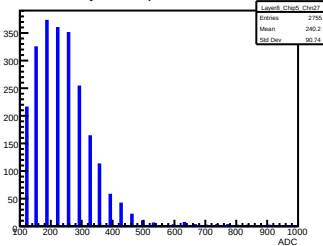
Layer 8, Chip 5, Channel 25



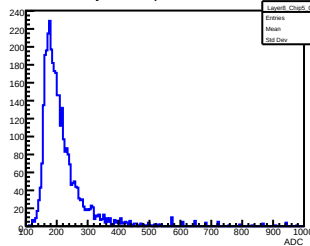
Layer 8, Chip 5, Channel 26



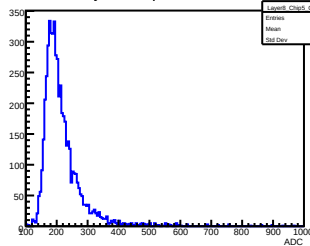
Layer 8, Chip 5, Channel 27



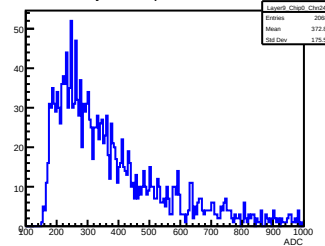
Layer 8, Chip 5, Channel 28



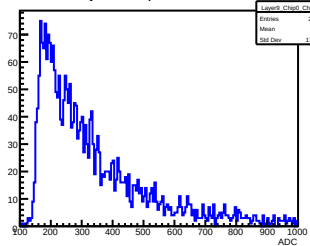
Layer 8, Chip 5, Channel 29



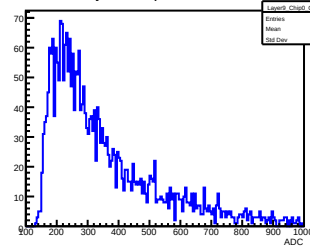
Layer 9, Chip 0, Channel 24



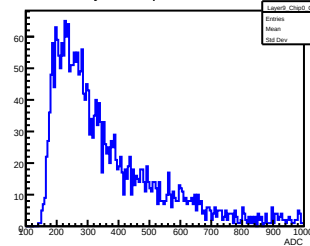
Layer 9, Chip 0, Channel 26



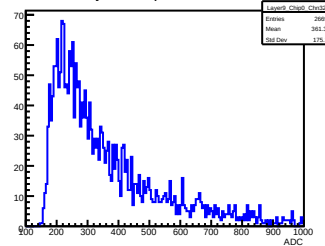
Layer 9, Chip 0, Channel 28



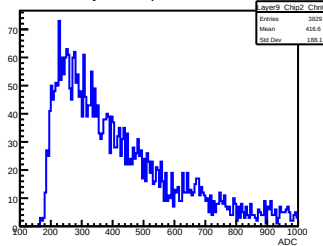
Layer 9, Chip 0, Channel 30



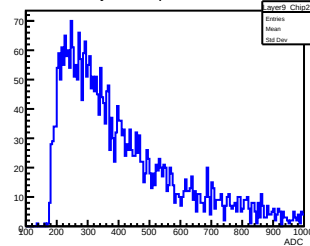
Layer 9, Chip 0, Channel 32



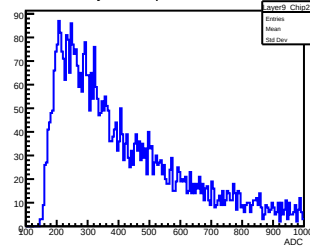
Layer 9, Chip 2, Channel 0



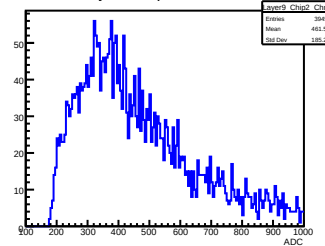
Layer 9, Chip 2, Channel 1



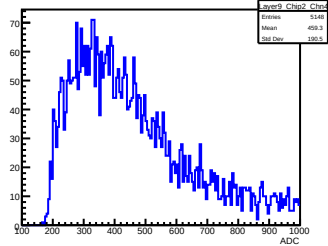
Layer 9, Chip 2, Channel 2



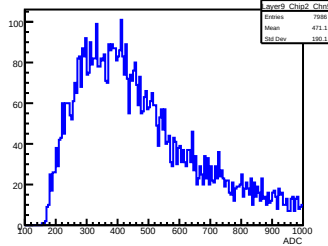
Layer 9, Chip 2, Channel 3



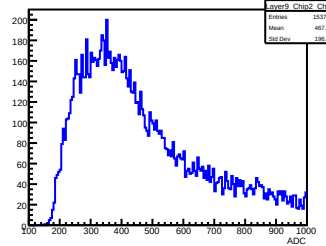
Layer 9, Chip 2, Channel 4



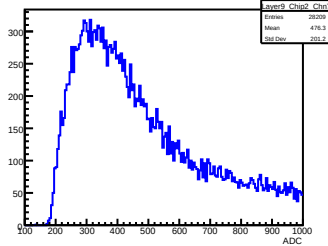
Layer 9, Chip 2, Channel 5



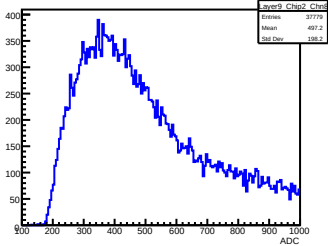
Layer 9, Chip 2, Channel 6



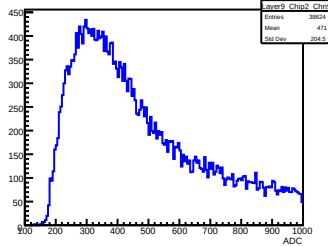
Layer 9, Chip 2, Channel 7



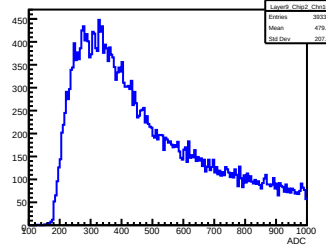
Layer 9, Chip 2, Channel 8



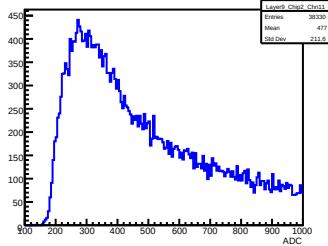
Layer 9, Chip 2, Channel 9



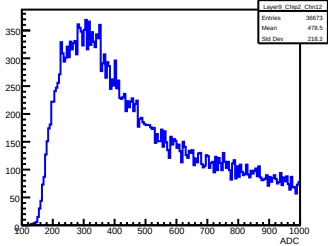
Layer 9, Chip 2, Channel 10



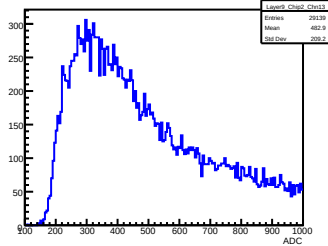
Layer 9, Chip 2, Channel 11



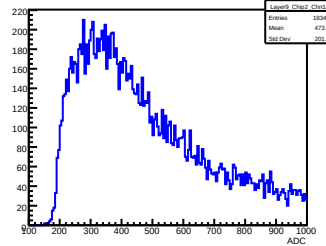
Layer 9, Chip 2, Channel 12



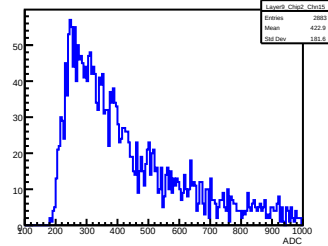
Layer 9, Chip 2, Channel 13



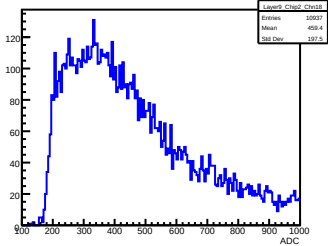
Layer 9, Chip 2, Channel 14



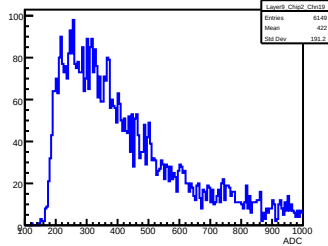
Layer 9, Chip 2, Channel 15



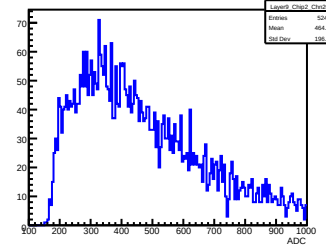
Layer 9, Chip 2, Channel 18



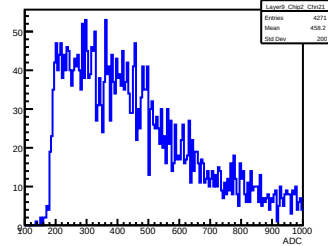
Layer 9, Chip 2, Channel 19



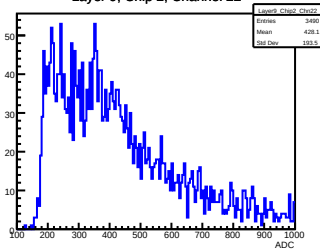
Layer 9, Chip 2, Channel 20



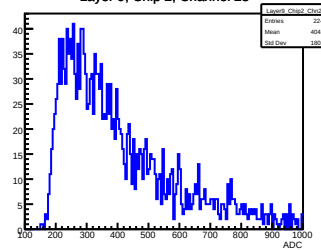
Layer 9, Chip 2, Channel 21



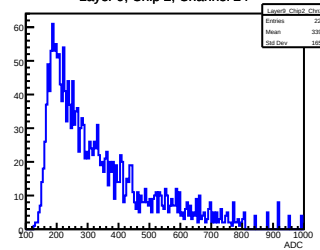
Layer 9, Chip 2, Channel 22



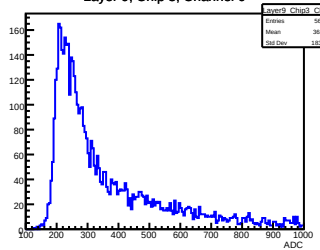
Layer 9, Chip 2, Channel 23



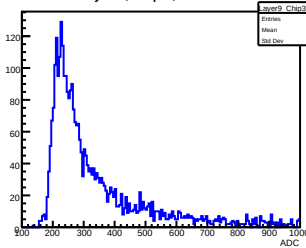
Layer 9, Chip 2, Channel 24



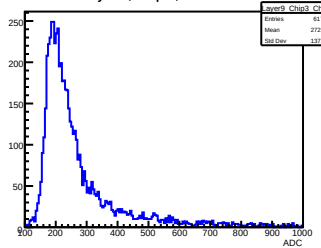
Layer 9, Chip 3, Channel 0



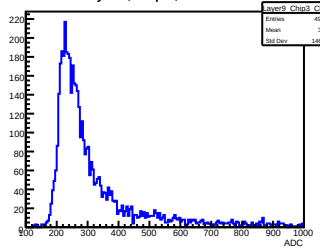
Layer 9, Chip 3, Channel 2



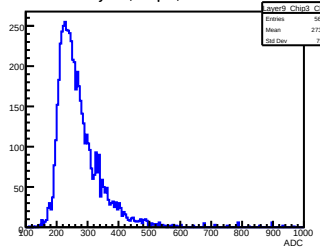
Layer 9, Chip 3, Channel 4



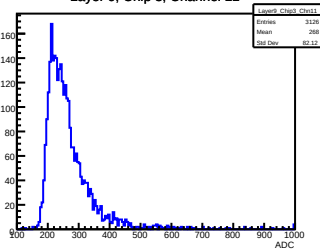
Layer 9, Chip 3, Channel 6



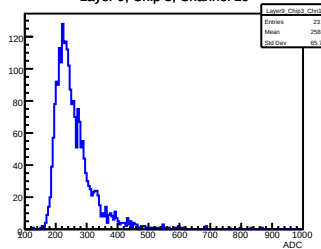
Layer 9, Chip 3, Channel 9



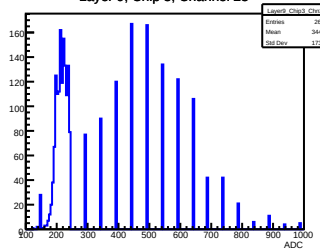
Layer 9, Chip 3, Channel 11



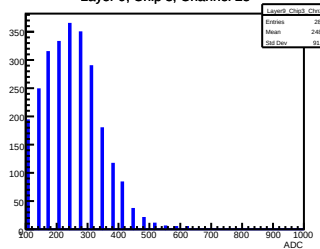
Layer 9, Chip 3, Channel 19



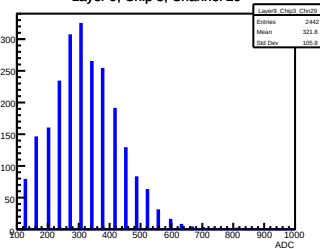
Layer 9, Chip 3, Channel 23



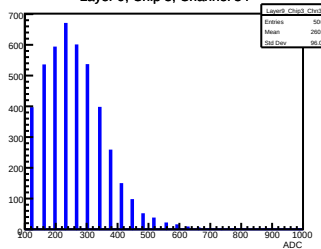
Layer 9, Chip 3, Channel 25



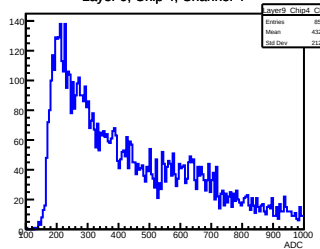
Layer 9, Chip 3, Channel 29



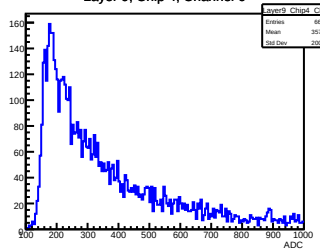
Layer 9, Chip 3, Channel 34



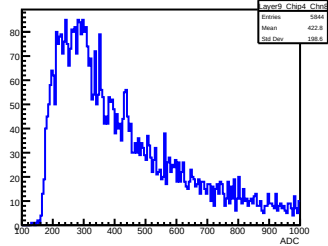
Layer 9, Chip 4, Channel 4



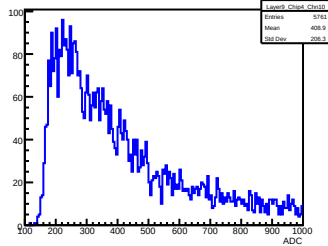
Layer 9, Chip 4, Channel 6



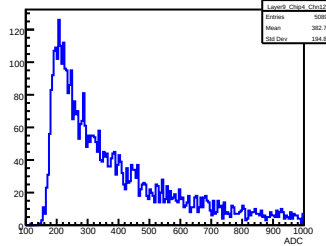
Layer 9, Chip 4, Channel 8



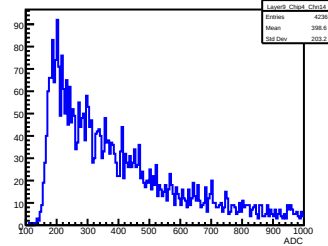
Layer 9, Chip 4, Channel 10



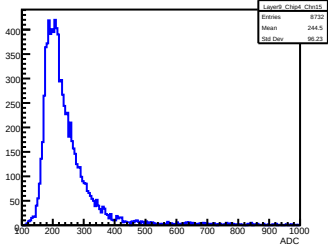
Layer 9, Chip 4, Channel 12



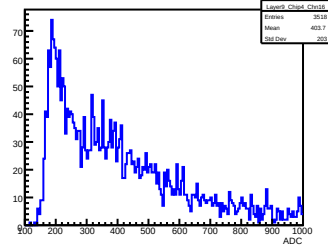
Layer 9, Chip 4, Channel 14



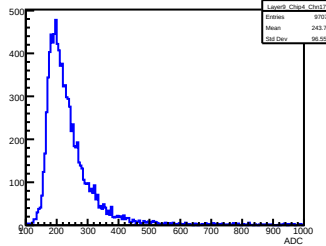
Layer 9, Chip 4, Channel 15



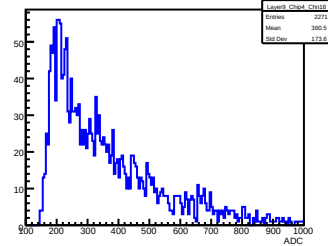
Layer 9, Chip 4, Channel 16



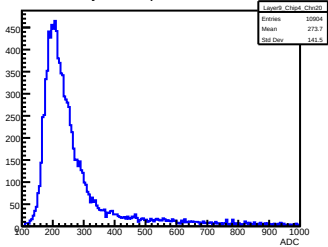
Layer 9, Chip 4, Channel 17



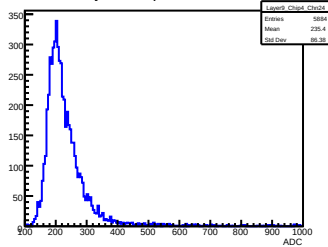
Layer 9, Chip 4, Channel 18



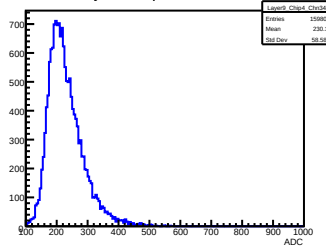
Layer 9, Chip 4, Channel 20



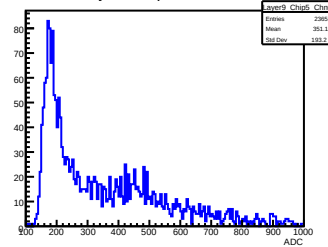
Layer 9, Chip 4, Channel 24



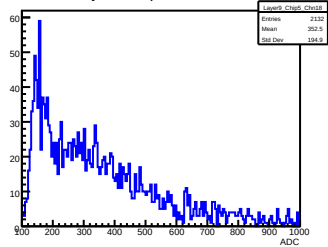
Layer 9, Chip 4, Channel 34



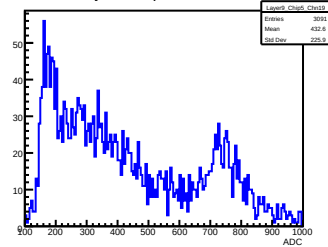
Layer 9, Chip 5, Channel 0



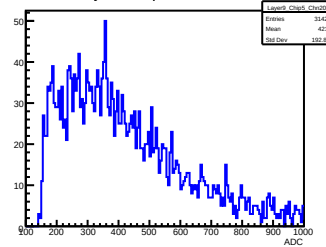
Layer 9, Chip 5, Channel 18



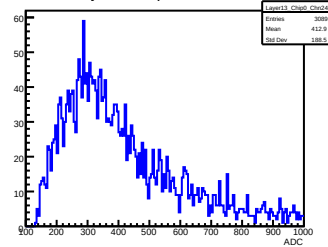
Layer 9, Chip 5, Channel 19



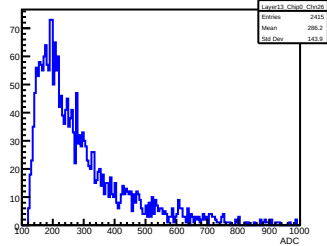
Layer 9, Chip 5, Channel 20



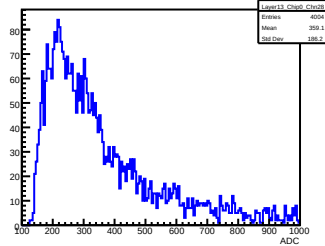
Layer 13, Chip 0, Channel 24



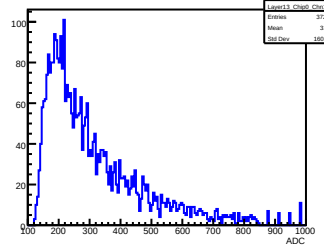
Layer 13, Chip 0, Channel 26



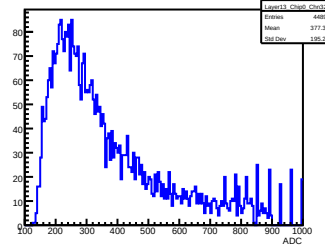
Layer 13, Chip 0, Channel 28



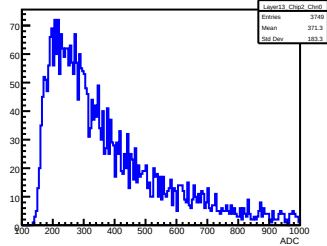
Layer 13, Chip 0, Channel 30



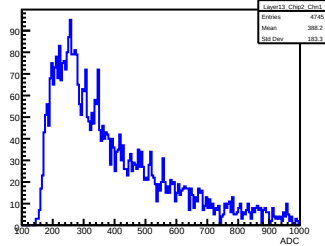
Layer 13, Chip 0, Channel 32



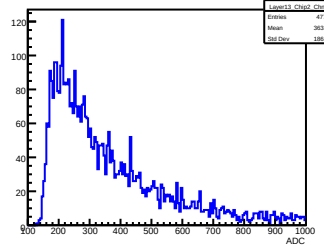
Layer 13, Chip 2, Channel 0



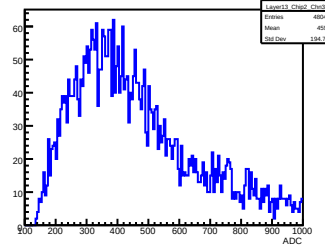
Layer 13, Chip 2, Channel 1



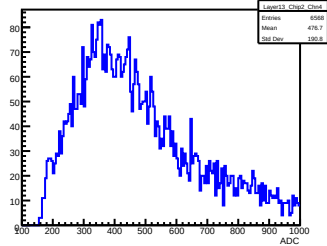
Layer 13, Chip 2, Channel 2



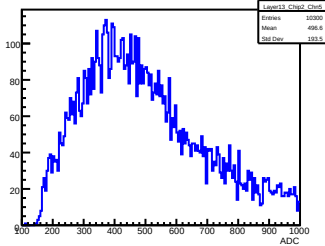
Layer 13, Chip 2, Channel 3



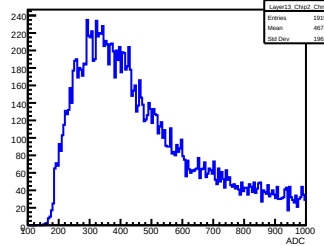
Layer 13, Chip 2, Channel 4



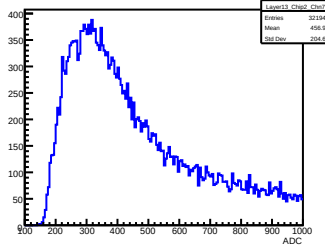
Layer 13, Chip 2, Channel 5



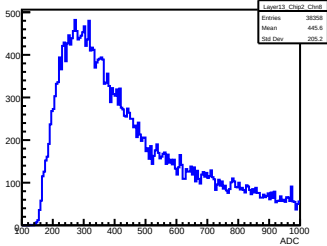
Layer 13, Chip 2, Channel 6



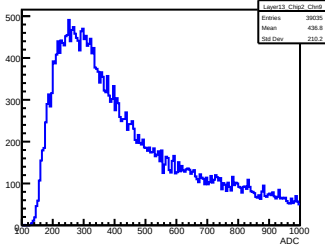
Layer 13, Chip 2, Channel 7



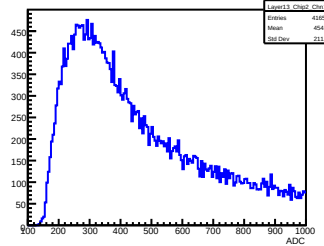
Layer 13, Chip 2, Channel 8



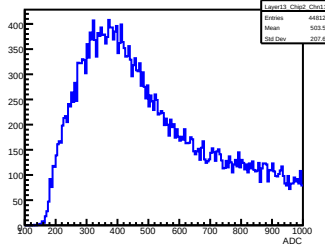
Layer 13, Chip 2, Channel 9



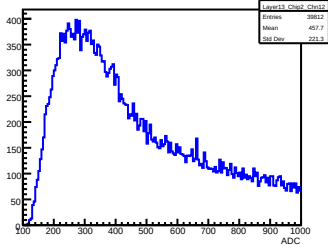
Layer 13, Chip 2, Channel 10



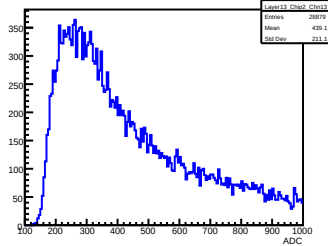
Layer 13, Chip 2, Channel 11



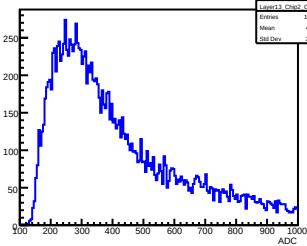
Layer 13, Chip 2, Channel 12



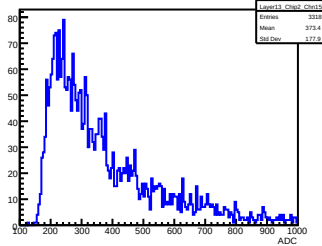
Layer 13, Chip 2, Channel 13



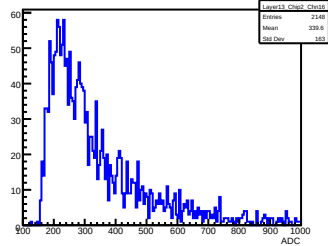
Layer 13, Chip 2, Channel 14



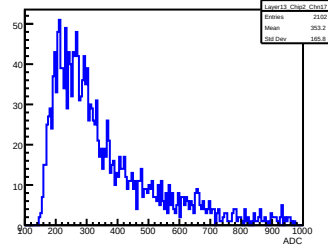
Layer 13, Chip 2, Channel 15



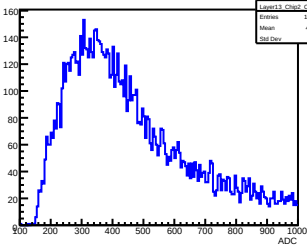
Layer 13, Chip 2, Channel 16



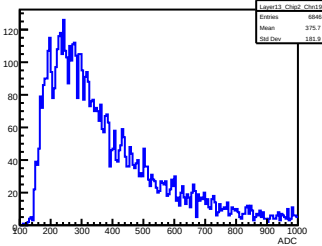
Layer 13, Chip 2, Channel 17



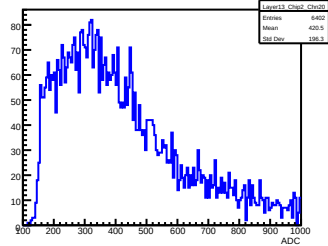
Layer 13, Chip 2, Channel 18



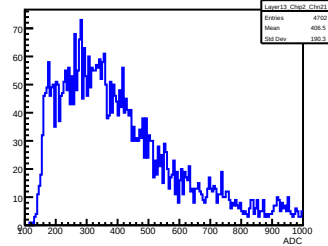
Layer 13, Chip 2, Channel 19



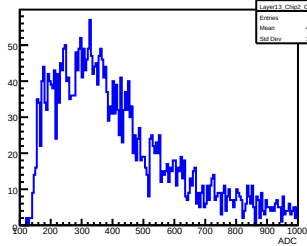
Layer 13, Chip 2, Channel 20



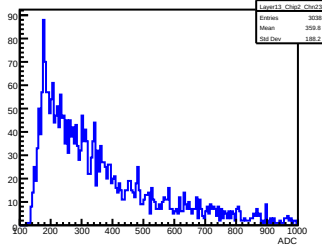
Layer 13, Chip 2, Channel 21



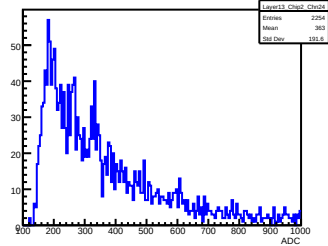
Layer 13, Chip 2, Channel 22



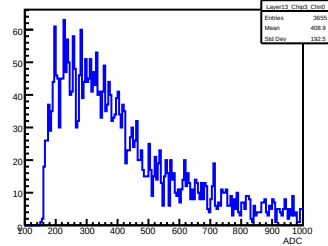
Layer 13, Chip 2, Channel 23



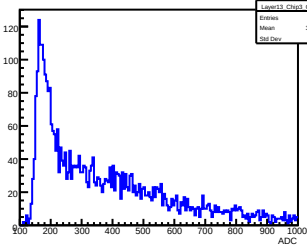
Layer 13, Chip 2, Channel 24



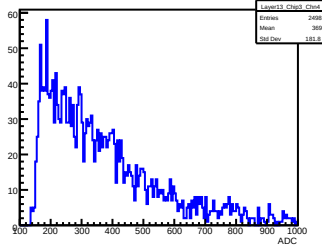
Layer 13, Chip 3, Channel 0



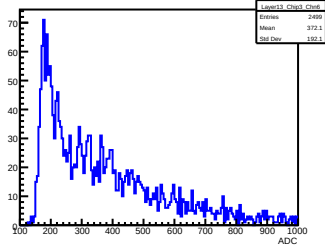
Layer 13, Chip 3, Channel 2



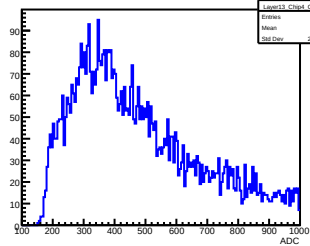
Layer 13, Chip 3, Channel 4



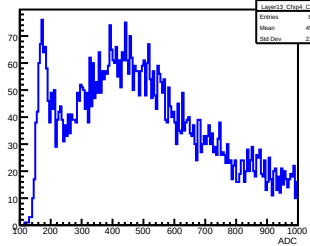
Layer 13, Chip 3, Channel 6



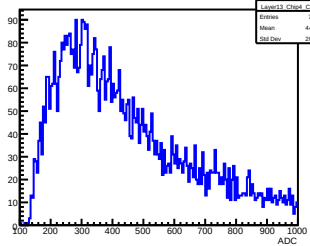
Layer 13, Chip 4, Channel 4



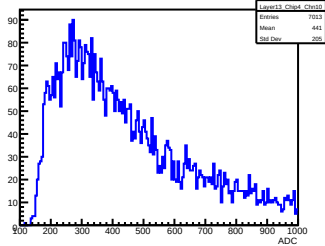
Layer 13, Chip 4, Channel 6



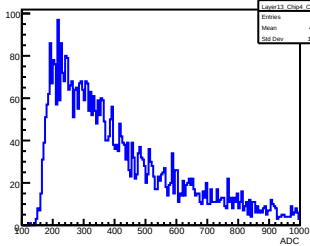
Layer 13, Chip 4, Channel 8



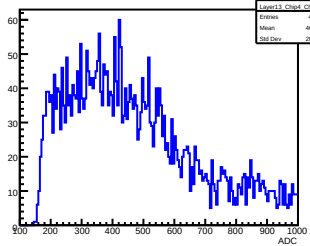
Layer 13, Chip 4, Channel 10



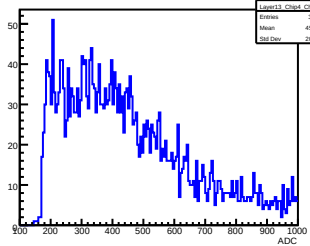
Layer 13, Chip 4, Channel 12



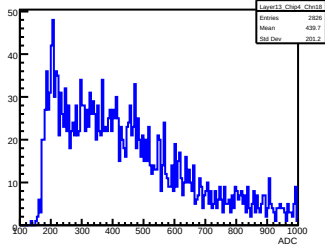
Layer 13, Chip 4, Channel 14



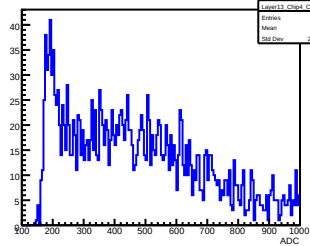
Layer 13, Chip 4, Channel 16



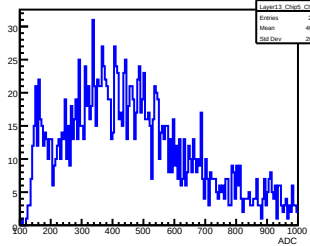
Layer 13, Chip 4, Channel 18



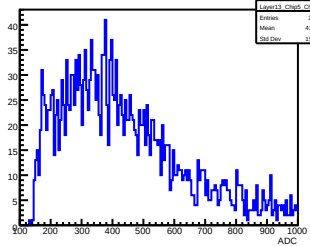
Layer 13, Chip 4, Channel 20



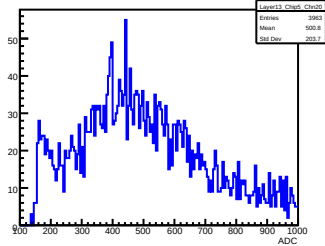
Layer 13, Chip 5, Channel 18



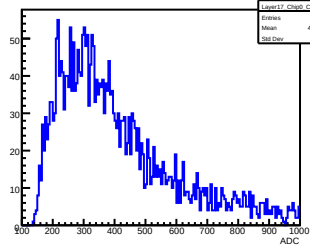
Layer 13, Chip 5, Channel 19



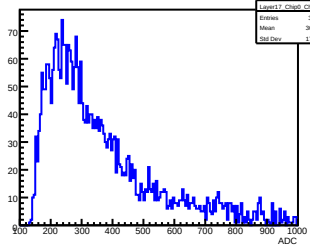
Layer 13, Chip 5, Channel 20



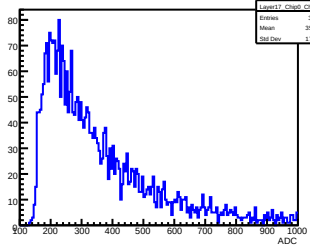
Layer 17, Chip 0, Channel 24



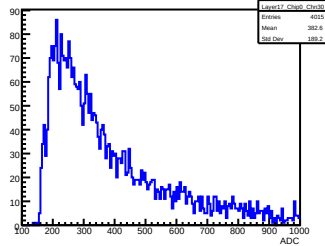
Layer 17, Chip 0, Channel 26



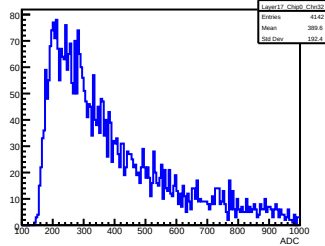
Layer 17, Chip 0, Channel 28



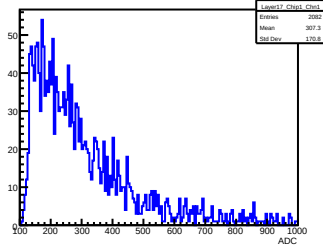
Layer 17, Chip 0, Channel 30



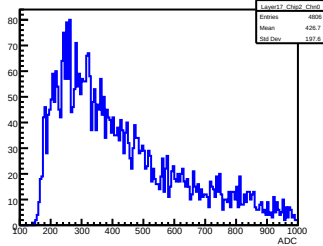
Layer 17, Chip 0, Channel 32



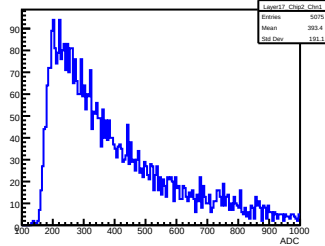
Layer 17, Chip 1, Channel 1



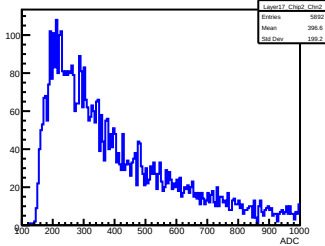
Layer 17, Chip 1, Channel 0



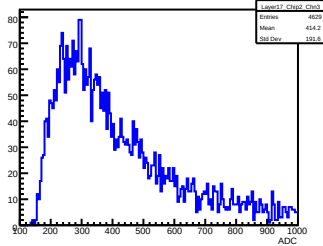
Layer 17, Chip 2, Channel 1



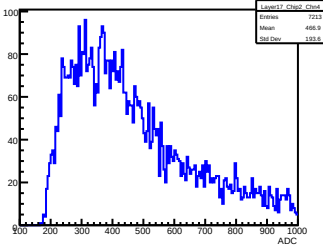
Layer 17, Chip 2, Channel 2



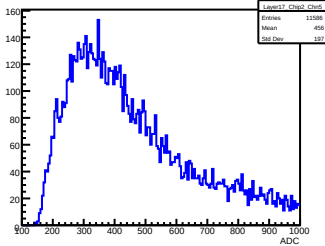
Layer 17, Chip 2, Channel 3



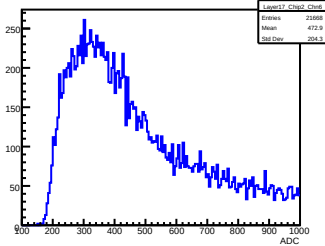
Layer 17, Chip 2, Channel 4



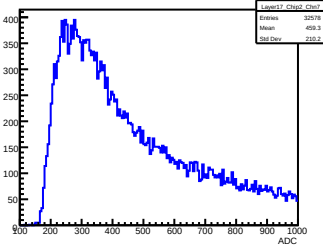
Layer 17, Chip 2, Channel 5



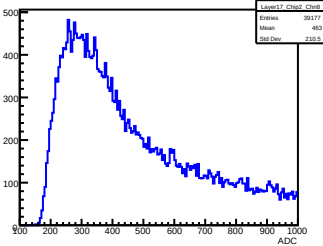
Layer 17, Chip 2, Channel 6



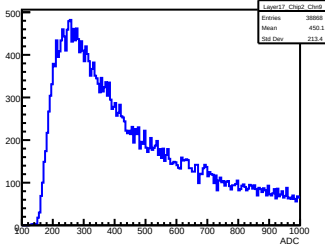
Layer 17, Chip 2, Channel 7



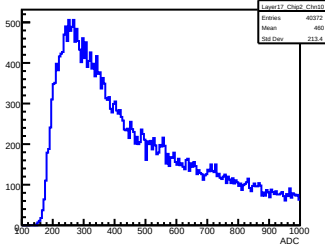
Layer 17, Chip 2, Channel 8



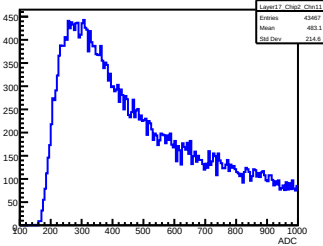
Layer 17, Chip 2, Channel 9



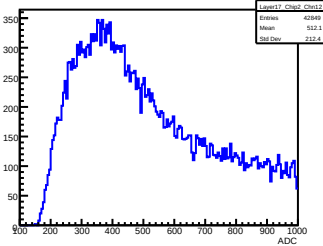
Layer 17, Chip 2, Channel 10



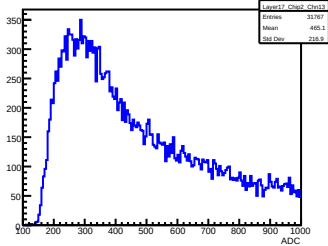
Layer 17, Chip 2, Channel 11



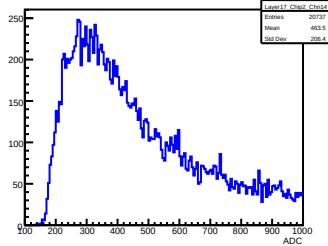
Layer 17, Chip 2, Channel 12



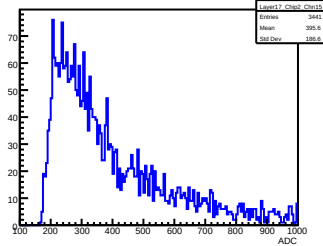
Layer 17, Chip 2, Channel 13



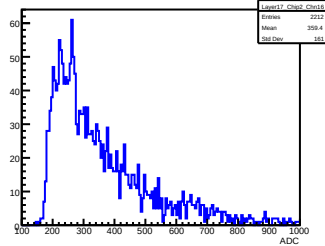
Layer 17, Chip 2, Channel 14



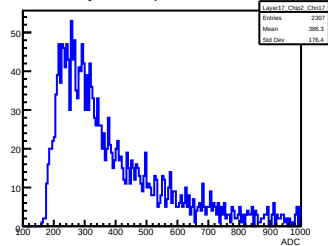
Layer 17, Chip 2, Channel 15



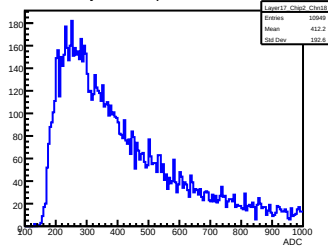
Layer 17, Chip 2, Channel 16



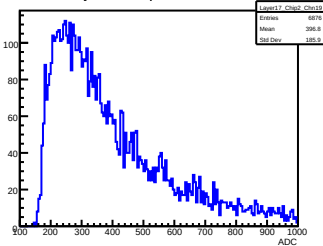
Layer 17, Chip 2, Channel 17



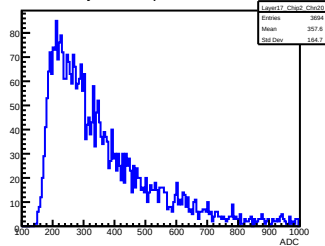
Layer 17, Chip 2, Channel 18



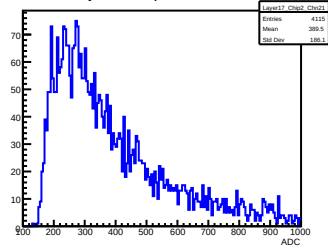
Layer 17, Chip 2, Channel 19



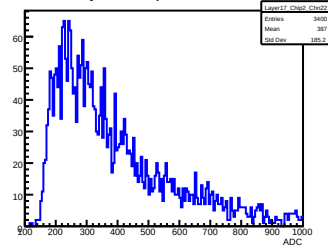
Layer 17, Chip 2, Channel 20



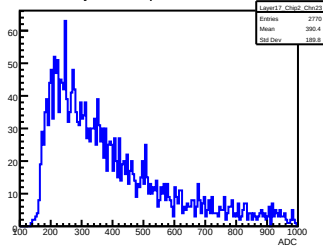
Layer 17, Chip 2, Channel 21



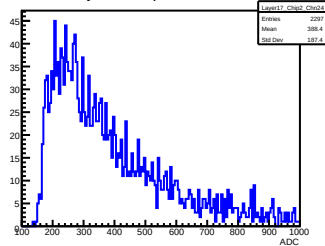
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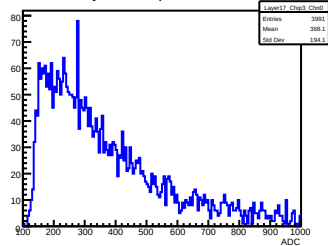
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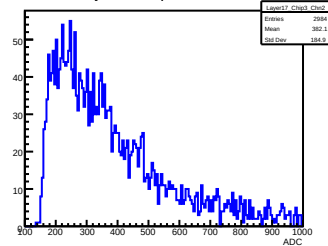
Layer 17, Chip 2, Channel 24



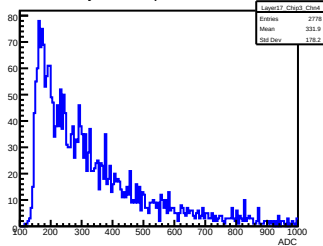
Layer 17, Chip 3, Channel 0



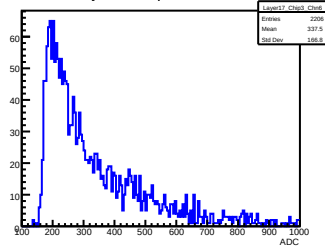
Layer 17, Chip 3, Channel 2



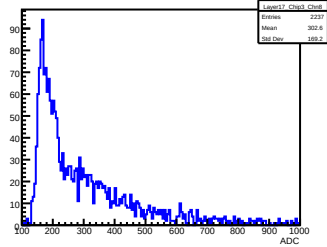
Layer 17, Chip 3, Channel 4



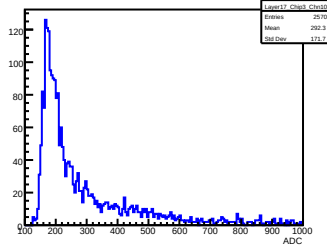
Layer 17, Chip 3, Channel 6



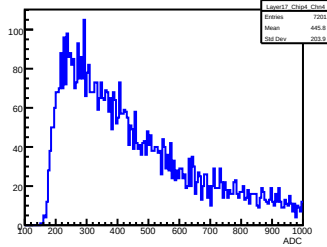
Layer 17, Chip 3, Channel 8



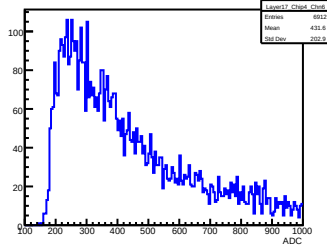
Layer 17, Chip 3, Channel 10



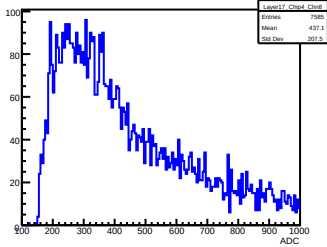
Layer 17, Chip 4, Channel 6



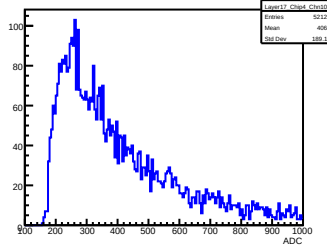
Layer 17, Chip 4, Channel 6



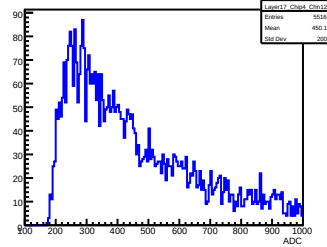
Layer 17, Chip 4, Channel 8



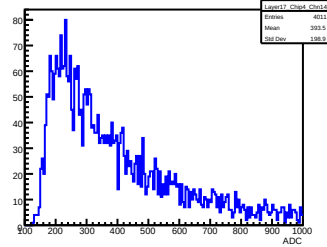
Layer 17, Chip 4, Channel 10



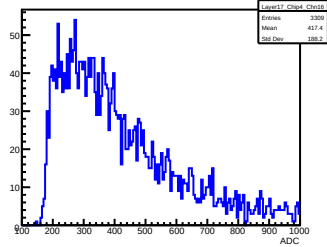
Layer 17, Chip 4, Channel 12



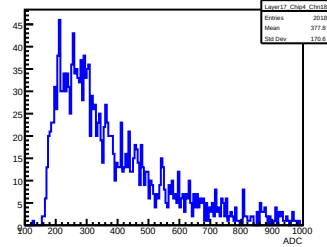
Layer 17, Chip 4, Channel 14



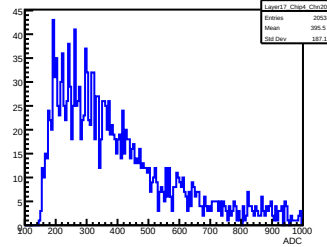
Layer 17, Chip 4, Channel 16



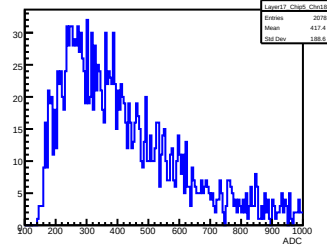
Layer 17, Chip 4, Channel 18



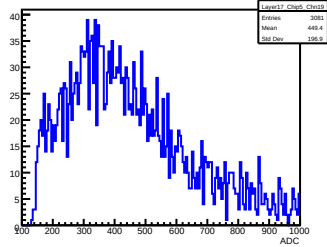
Layer 17, Chip 4, Channel 20



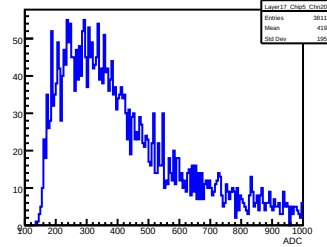
Layer 17, Chip 5, Channel 18



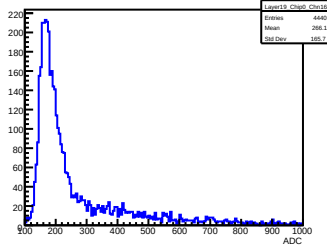
Layer 17, Chip 5, Channel 19



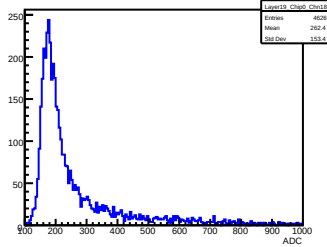
Layer 17, Chip 5, Channel 20



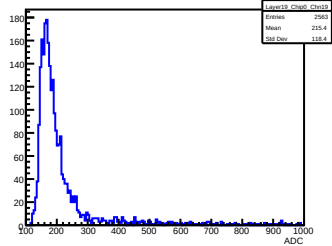
Layer 19, Chip 0, Channel 16



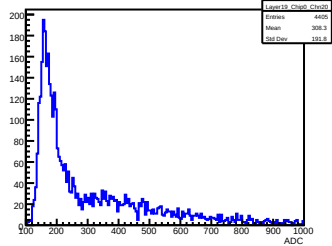
Layer 19, Chip 0, Channel 18



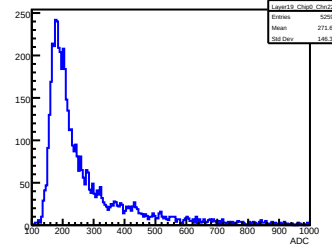
Layer 19, Chip 0, Channel 19



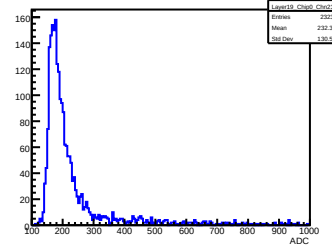
Layer 19, Chip 0, Channel 20



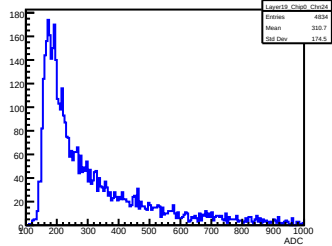
Layer 19, Chip 0, Channel 22



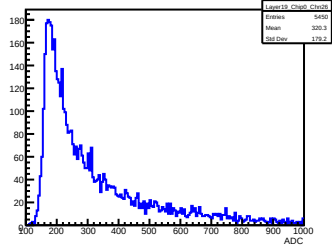
Layer 19, Chip 0, Channel 23



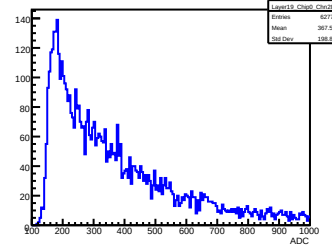
Layer 19, Chip 0, Channel 24



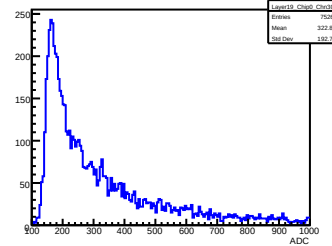
Layer 19, Chip 0, Channel 26



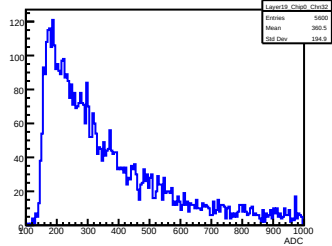
Layer 19, Chip 0, Channel 28



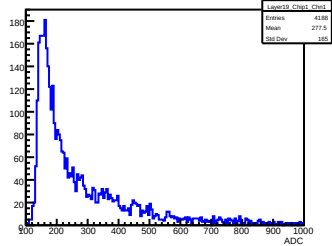
Layer 19, Chip 0, Channel 30



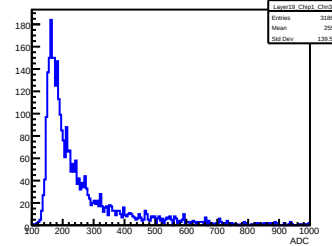
Layer 19, Chip 0, Channel 32



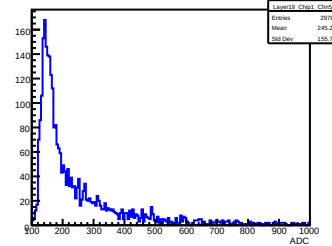
Layer 19, Chip 1, Channel 1



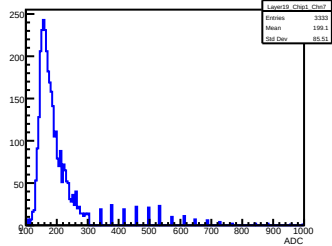
Layer 19, Chip 1, Channel 3



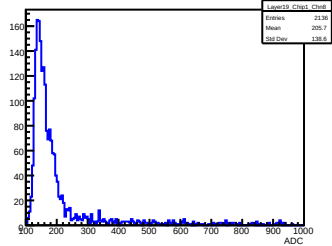
Layer 19, Chip 1, Channel 5



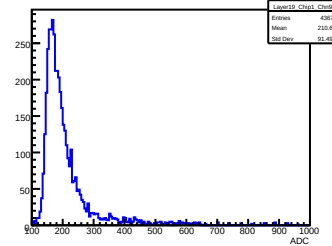
Layer 19, Chip 1, Channel 7



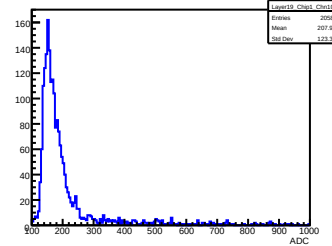
Layer 19, Chip 1, Channel 8



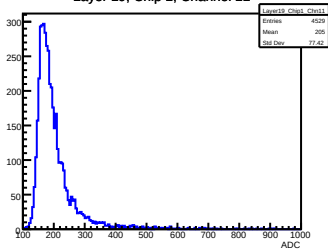
Layer 19, Chip 1, Channel 9



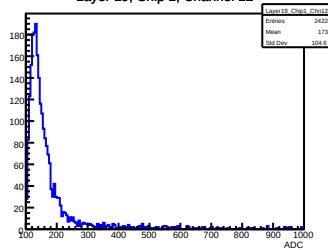
Layer 19, Chip 1, Channel 10



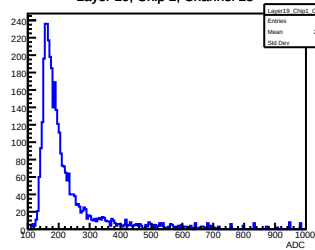
Layer 19, Chip 1, Channel 11



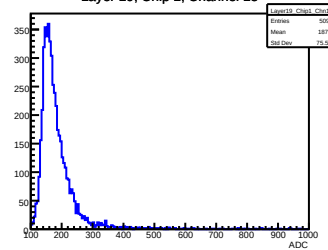
Layer 19, Chip 1, Channel 12



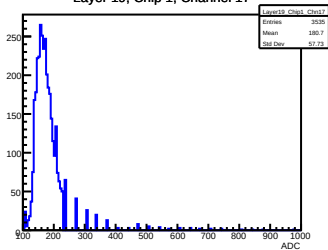
Layer 19, Chip 1, Channel 13



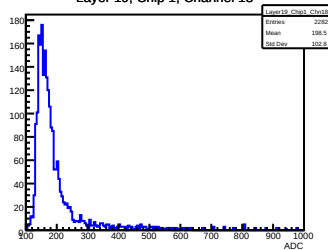
Layer 19, Chip 1, Channel 15



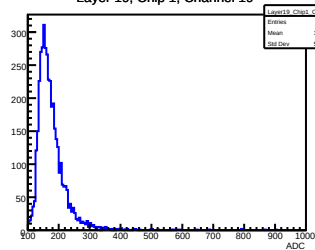
Layer 19, Chip 1, Channel 17



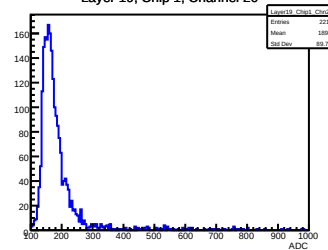
Layer 19, Chip 1, Channel 18



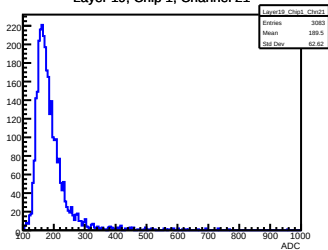
Layer 19, Chip 1, Channel 19



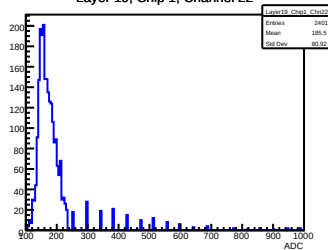
Layer 19, Chip 1, Channel 20



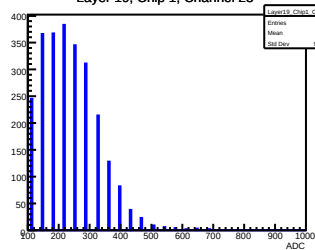
Layer 19, Chip 1, Channel 21



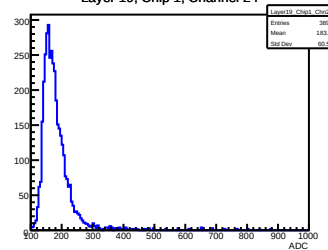
Layer 19, Chip 1, Channel 22



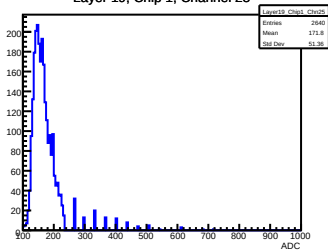
Layer 19, Chip 1, Channel 23



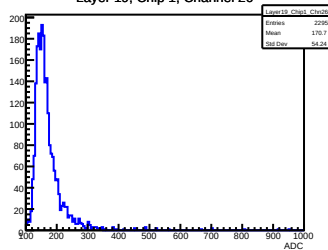
Layer 19, Chip 1, Channel 24



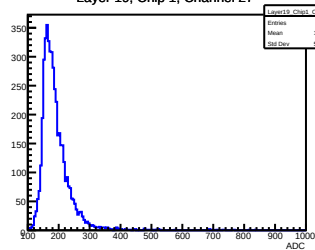
Layer 19, Chip 1, Channel 25



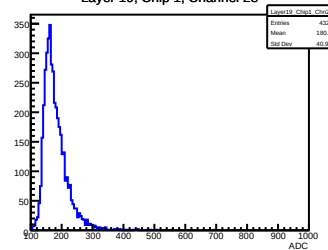
Layer 19, Chip 1, Channel 26



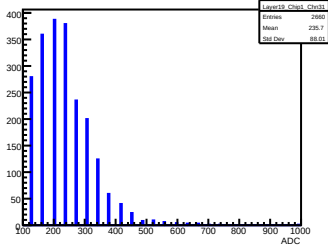
Layer 19, Chip 1, Channel 27



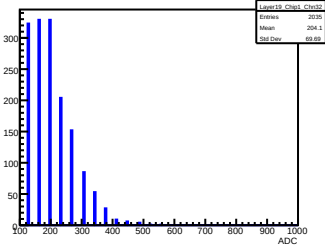
Layer 19, Chip 1, Channel 28



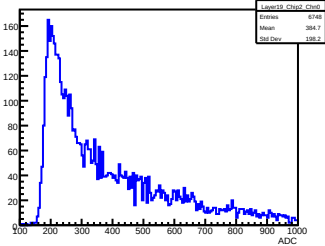
Layer 19, Chip 1, Channel 31



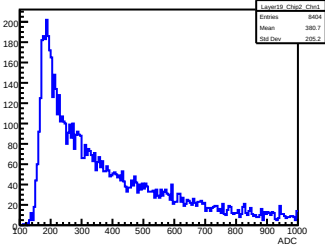
Layer 19, Chip 1, Channel 32



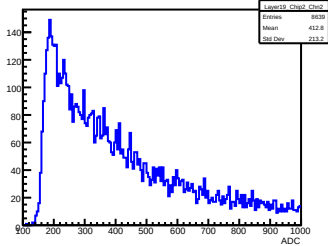
Layer 19, Chip 2, Channel 0



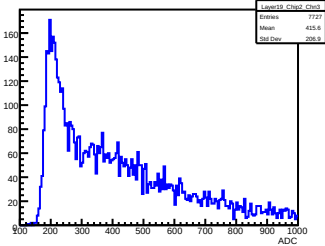
Layer 19, Chip 2, Channel 1



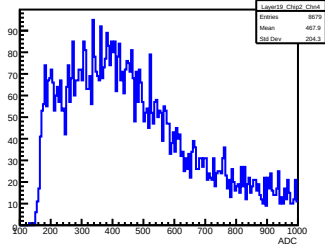
Layer 19, Chip 2, Channel 2



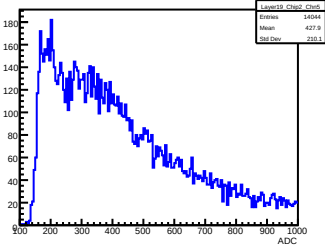
Layer 19, Chip 2, Channel 3



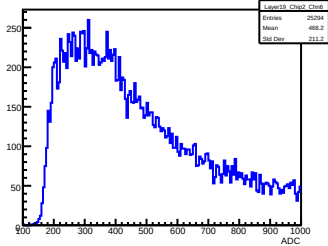
Layer 19, Chip 2, Channel 4



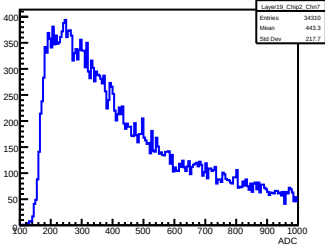
Layer 19, Chip 2, Channel 5



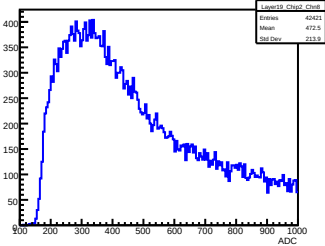
Layer 19, Chip 2, Channel 6



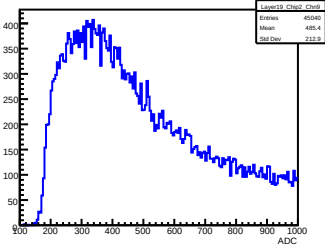
Layer 19, Chip 2, Channel 7



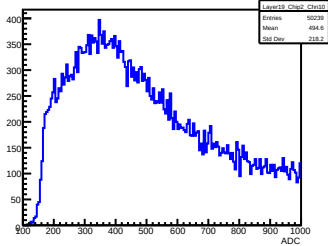
Layer 19, Chip 2, Channel 8



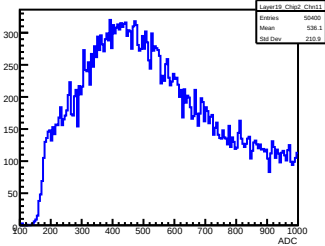
Layer 19, Chip 2, Channel 9



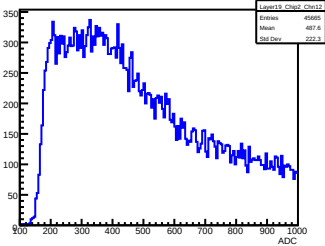
Layer 19, Chip 2, Channel 10



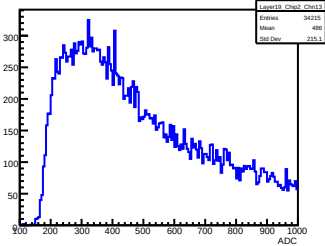
Layer 19, Chip 2, Channel 11



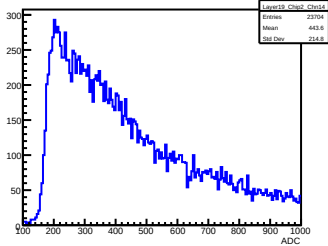
Layer 19, Chip 2, Channel 12



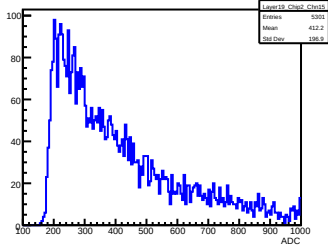
Layer 19, Chip 2, Channel 13



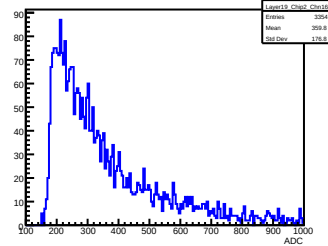
Layer 19, Chip 2, Channel 14



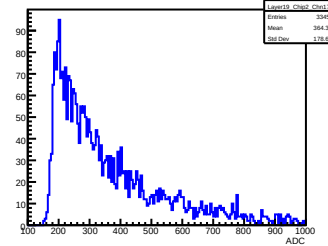
Layer 19, Chip 2, Channel 15



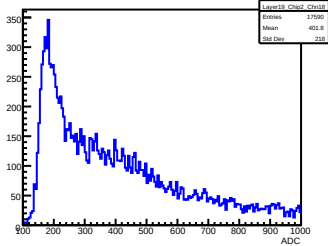
Layer 19, Chip 2, Channel 16



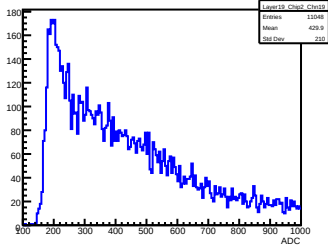
Layer 19, Chip 2, Channel 17



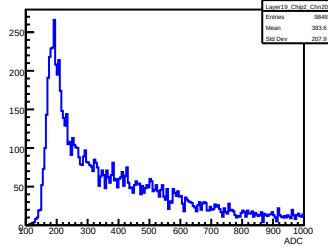
Layer 19, Chip 2, Channel 18



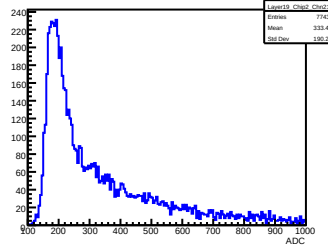
Layer 19, Chip 2, Channel 19



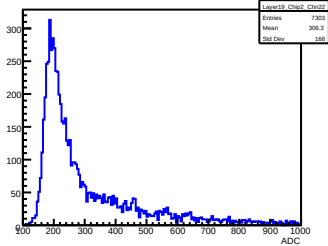
Layer 19, Chip 2, Channel 20



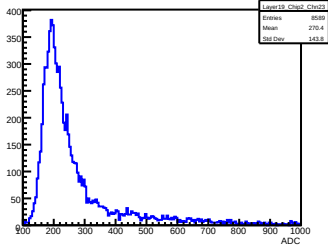
Layer 19, Chip 2, Channel 21



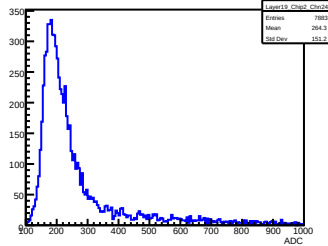
Layer 19, Chip 2, Channel 22



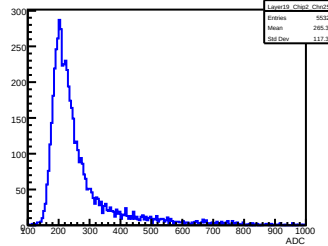
Layer 19, Chip 2, Channel 23



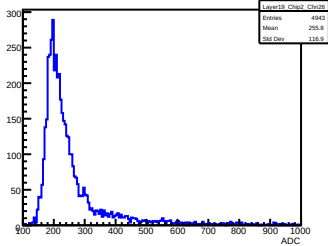
Layer 19, Chip 2, Channel 24



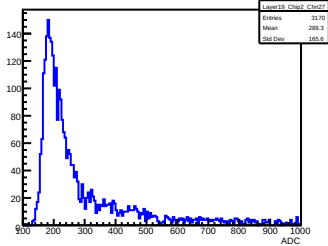
Layer 19, Chip 2, Channel 25



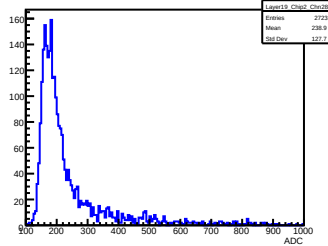
Layer 19, Chip 2, Channel 26



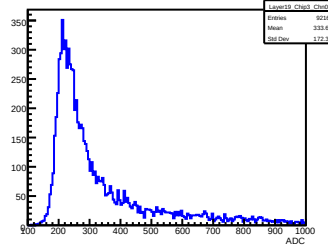
Layer 19, Chip 2, Channel 27



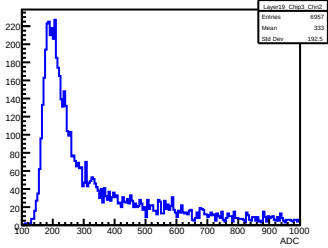
Layer 19, Chip 2, Channel 28



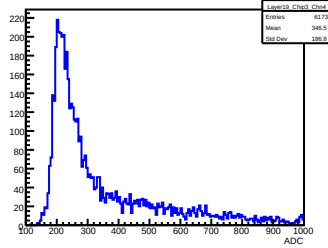
Layer 19, Chip 3, Channel 0



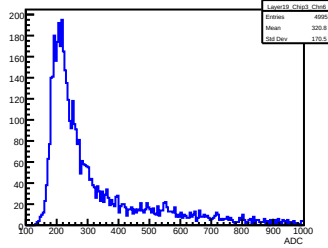
Layer 19, Chip 3, Channel 2



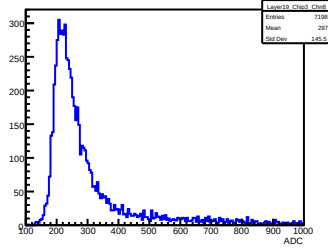
Layer 19, Chip 3, Channel 4



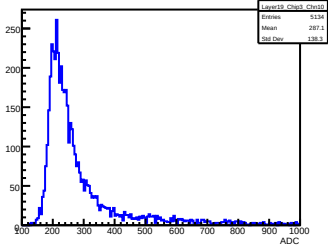
Layer 19, Chip 3, Channel 6



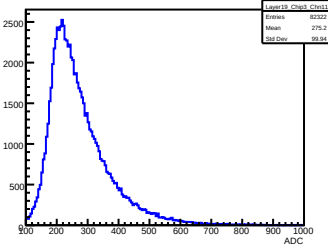
Layer 19, Chip 3, Channel 8



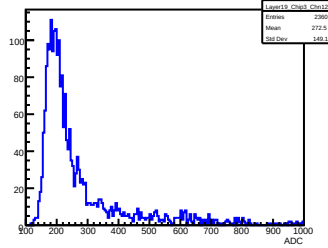
Layer 19, Chip 3, Channel 10



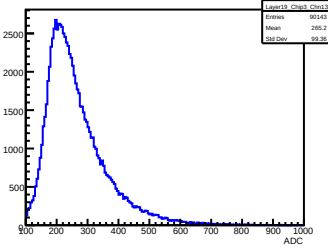
Layer 19, Chip 3, Channel 11



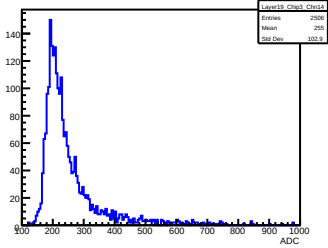
Layer 19, Chip 3, Channel 12



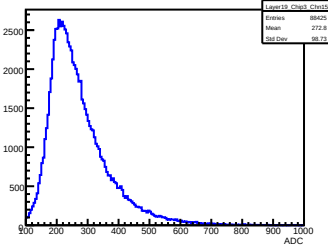
Layer 19, Chip 3, Channel 13



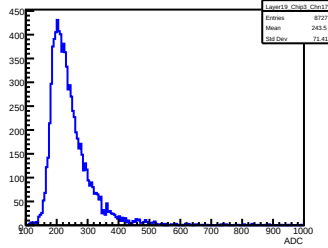
Layer 19, Chip 3, Channel 14



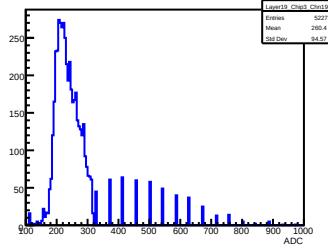
Layer 19, Chip 3, Channel 15



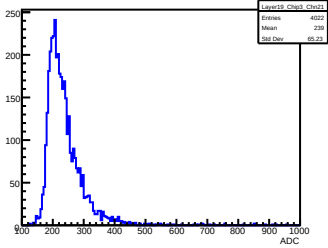
Layer 19, Chip 3, Channel 17



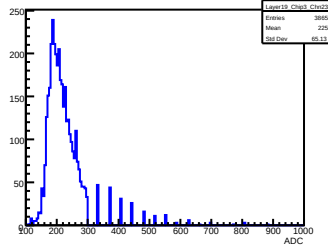
Layer 19, Chip 3, Channel 19



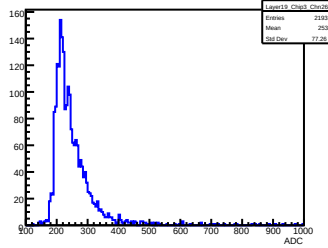
Layer 19, Chip 3, Channel 21



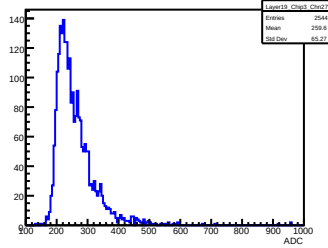
Layer 19, Chip 3, Channel 23



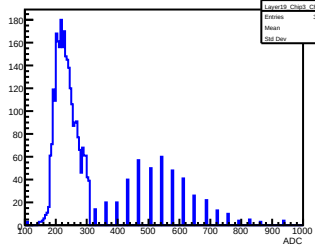
Layer 19, Chip 3, Channel 26



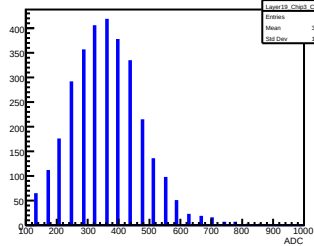
Layer 19, Chip 3, Channel 27



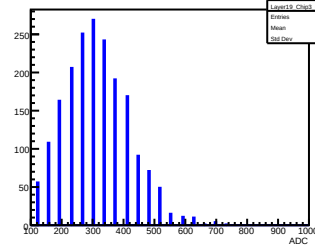
Layer 19, Chip 3, Channel 29



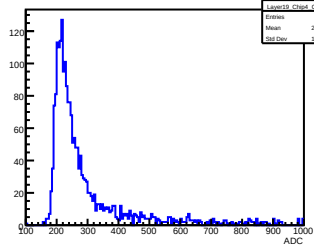
Layer 19, Chip 3, Channel 31



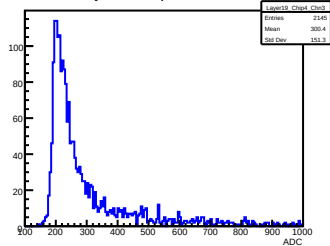
Layer 19, Chip 3, Channel 35



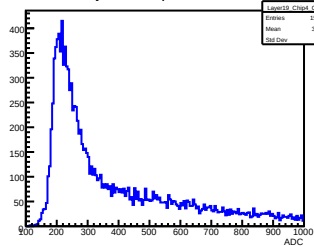
Layer 19, Chip 4, Channel 1



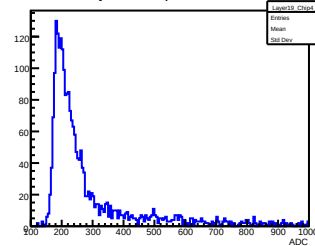
Layer 19, Chip 4, Channel 3



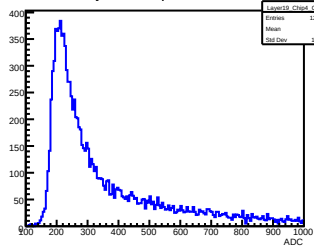
Layer 19, Chip 4, Channel 4



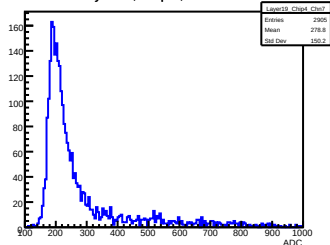
Layer 19, Chip 4, Channel 5



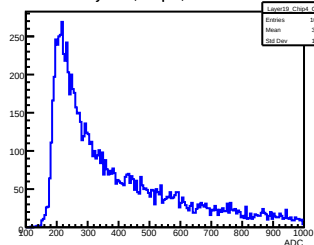
Layer 19, Chip 4, Channel 6



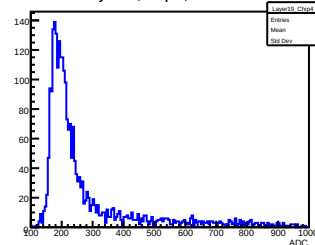
Layer 19, Chip 4, Channel 7



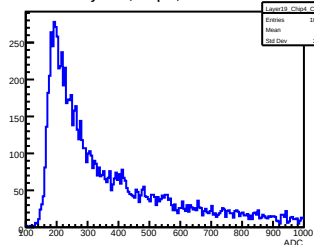
Layer 19, Chip 4, Channel 8



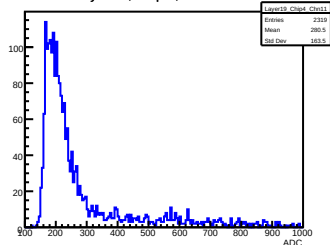
Layer 19, Chip 4, Channel 9



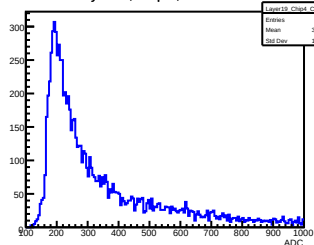
Layer 19, Chip 4, Channel 10



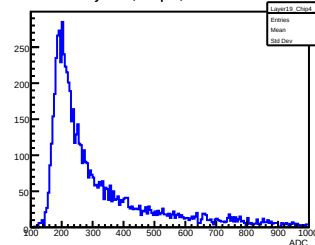
Layer 19, Chip 4, Channel 11



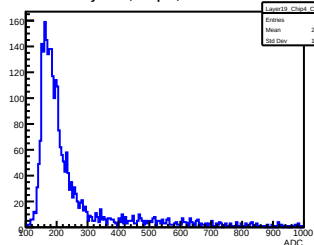
Layer 19, Chip 4, Channel 12



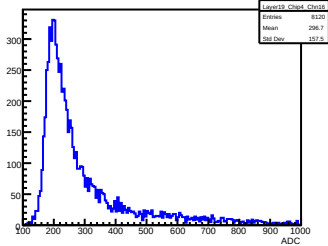
Layer 19, Chip 4, Channel 14



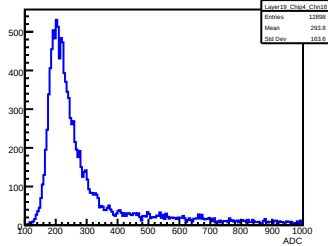
Layer 19, Chip 4, Channel 15



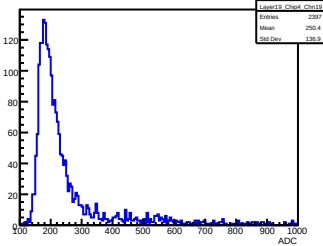
Layer 19, Chip 4, Channel 16



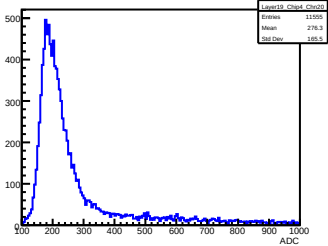
Layer 19, Chip 4, Channel 18



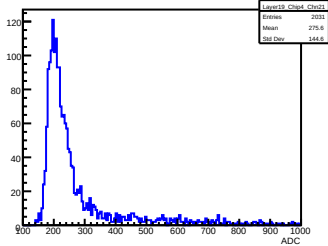
Layer 19, Chip 4, Channel 19



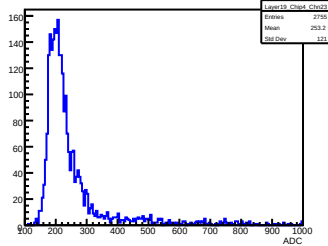
Layer 19, Chip 4, Channel 20



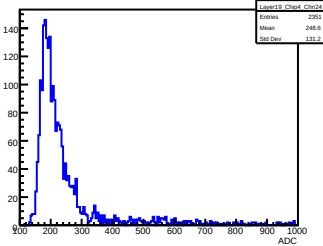
Layer 19, Chip 4, Channel 21



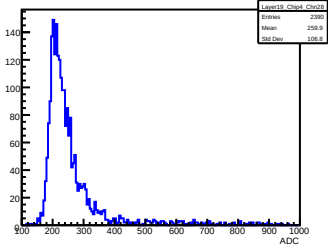
Layer 19, Chip 4, Channel 23



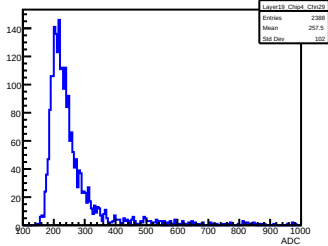
Layer 19, Chip 4, Channel 24



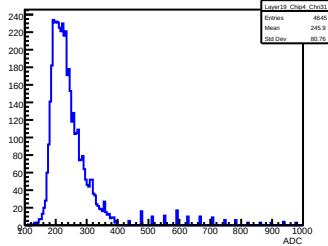
Layer 19, Chip 4, Channel 28



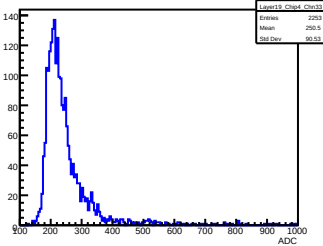
Layer 19, Chip 4, Channel 29



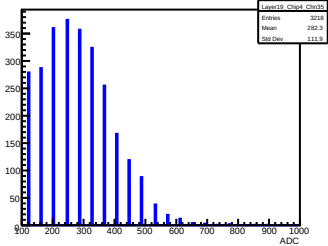
Layer 19, Chip 4, Channel 31



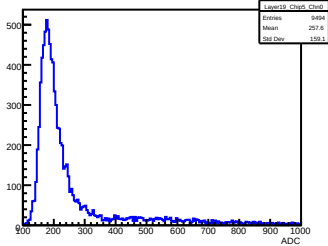
Layer 19, Chip 4, Channel 33



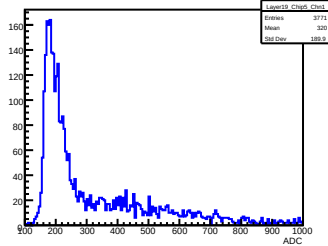
Layer 19, Chip 4, Channel 35



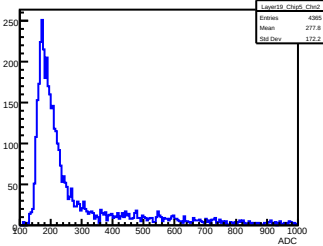
Layer 19, Chip 5, Channel 0



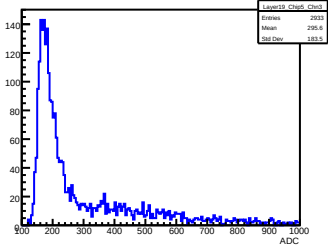
Layer 19, Chip 5, Channel 1



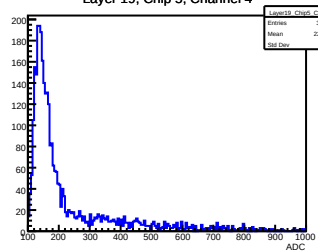
Layer 19, Chip 5, Channel 2



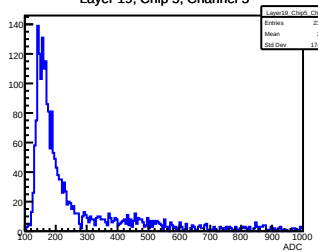
Layer 19, Chip 5, Channel 3



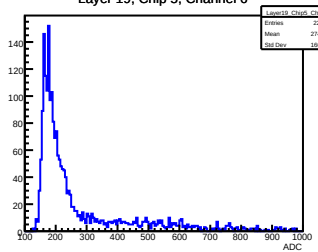
Layer 19, Chip 5, Channel 4



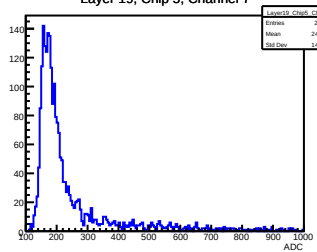
Layer 19, Chip 5, Channel 5



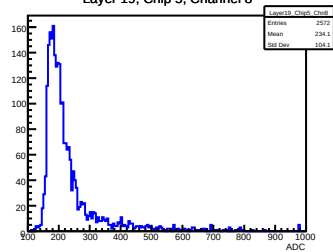
Layer 19, Chip 5, Channel 6



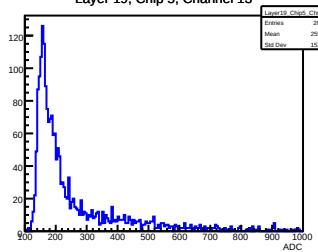
Layer 19, Chip 5, Channel 7



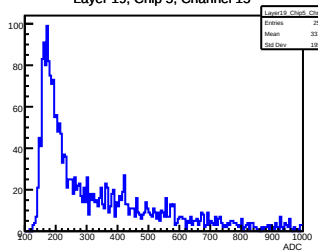
Layer 19, Chip 5, Channel 8



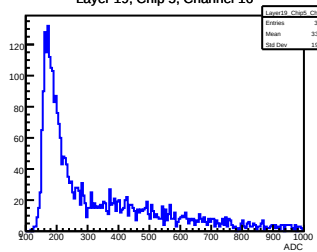
Layer 19, Chip 5, Channel 13



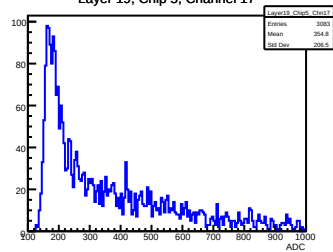
Layer 19, Chip 5, Channel 15



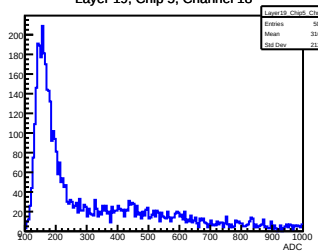
Layer 19, Chip 5, Channel 16



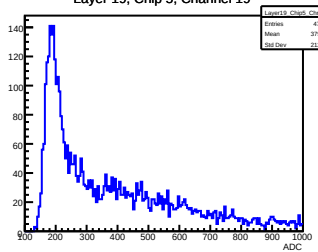
Layer 19, Chip 5, Channel 17



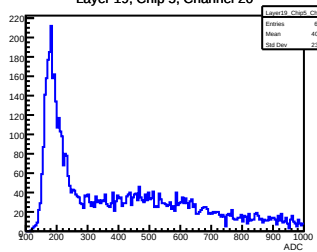
Layer 19, Chip 5, Channel 18



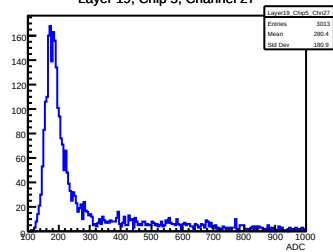
Layer 19, Chip 5, Channel 19



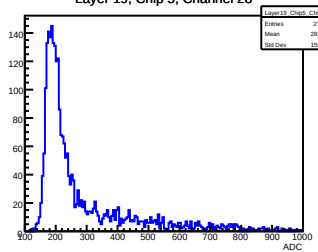
Layer 19, Chip 5, Channel 20



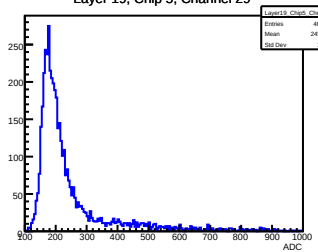
Layer 19, Chip 5, Channel 27



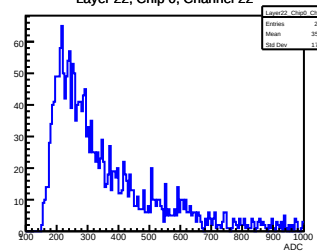
Layer 19, Chip 5, Channel 28



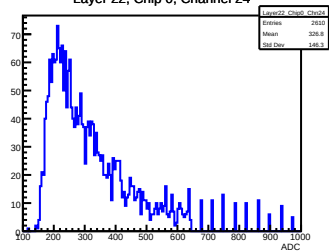
Layer 19, Chip 5, Channel 29



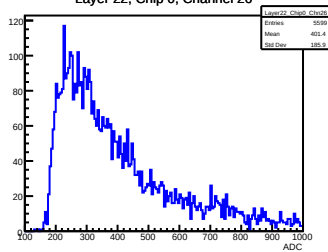
Layer 22, Chip 0, Channel 22



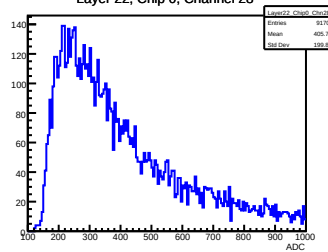
Layer 22, Chip 0, Channel 24



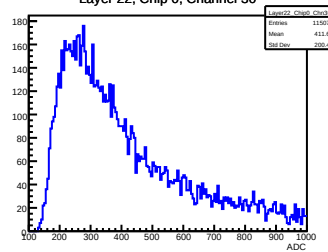
Layer 22, Chip 0, Channel 26



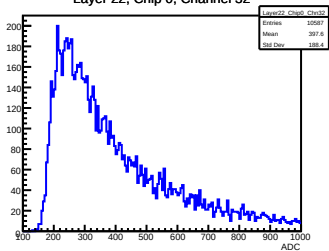
Layer 22, Chip 0, Channel 28



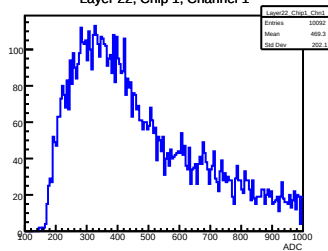
Layer 22, Chip 0, Channel 30



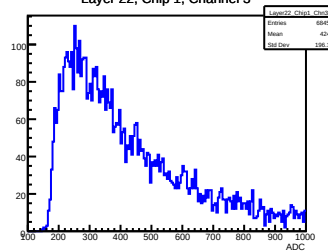
Layer 22, Chip 0, Channel 32



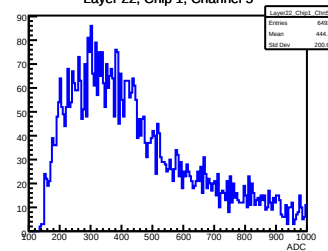
Layer 22, Chip 1, Channel 1



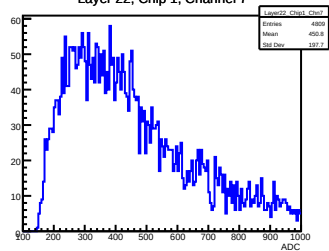
Layer 22, Chip 1, Channel 3



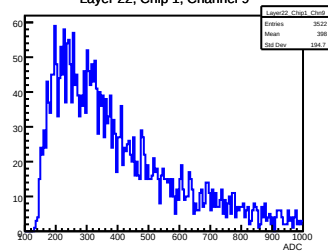
Layer 22, Chip 1, Channel 5



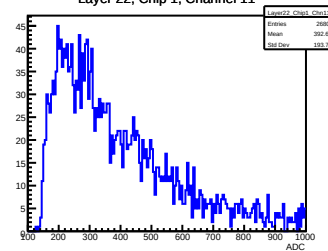
Layer 22, Chip 1, Channel 7



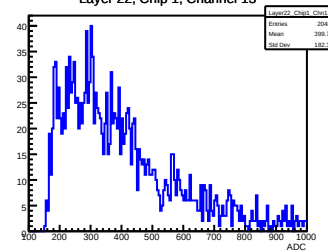
Layer 22, Chip 1, Channel 9



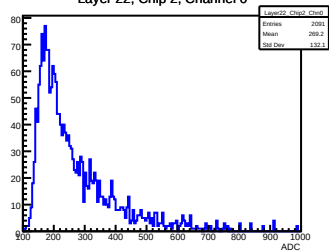
Layer 22, Chip 1, Channel 11



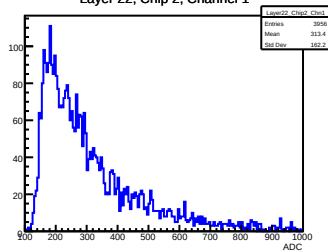
Layer 22, Chip 1, Channel 13



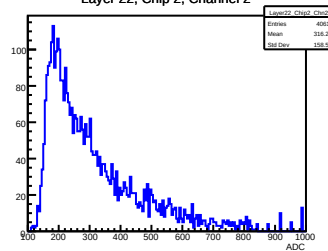
Layer 22, Chip 2, Channel 0



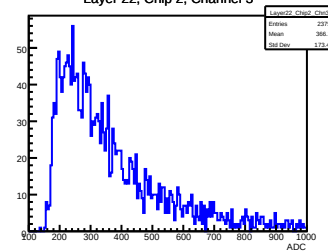
Layer 22, Chip 2, Channel 1



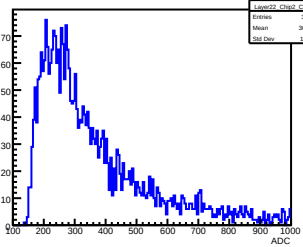
Layer 22, Chip 2, Channel 2



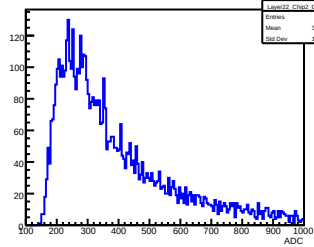
Layer 22, Chip 2, Channel 3



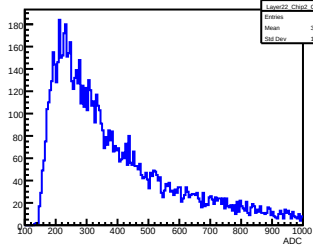
Layer 22, Chip 2, Channel 4



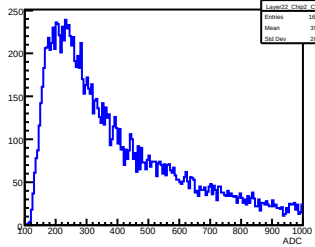
Layer 22, Chip 2, Channel 5



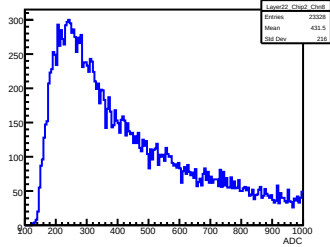
Layer 22, Chip 2, Channel 6



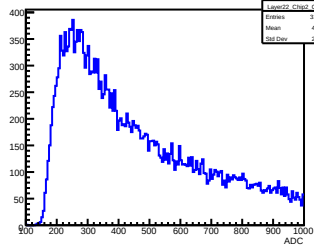
Layer 22, Chip 2, Channel 7



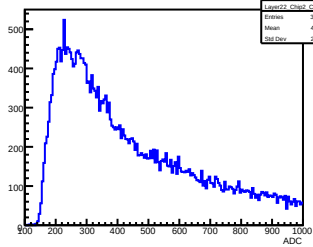
Layer 22, Chip 2, Channel 8



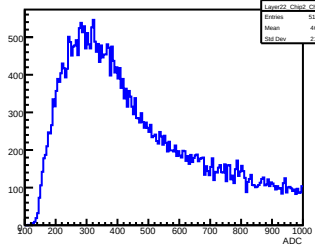
Layer 22, Chip 2, Channel 9



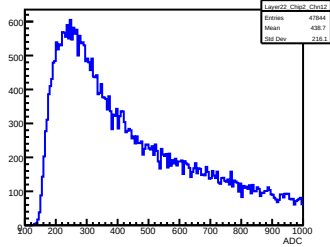
Layer 22, Chip 2, Channel 10



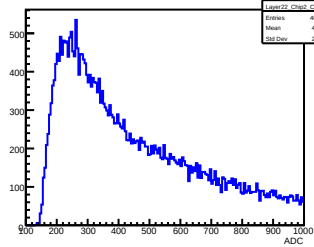
Layer 22, Chip 2, Channel 11



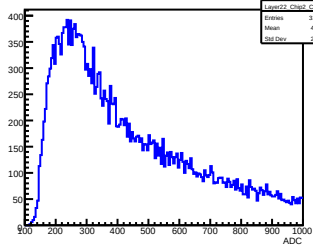
Layer 22, Chip 2, Channel 12



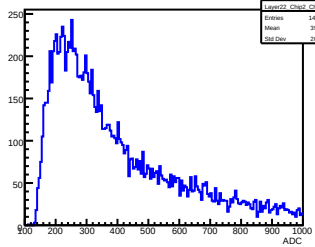
Layer 22, Chip 2, Channel 13



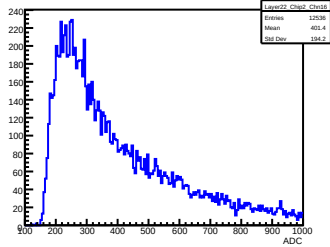
Layer 22, Chip 2, Channel 14



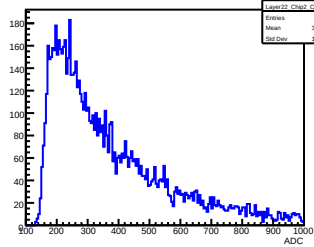
Layer 22, Chip 2, Channel 15



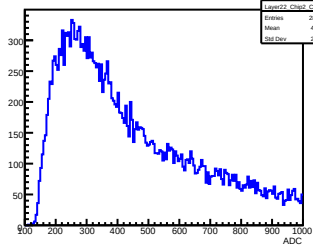
Layer 22, Chip 2, Channel 16



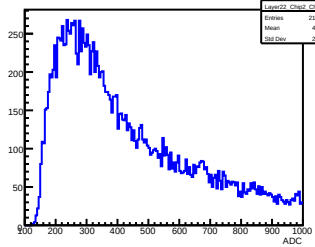
Layer 22, Chip 2, Channel 17



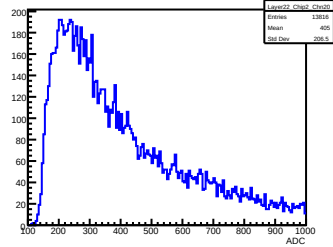
Layer 22, Chip 2, Channel 18



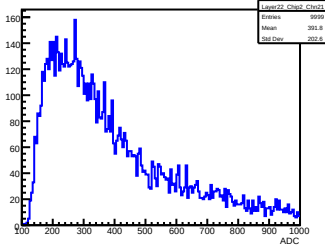
Layer 22, Chip 2, Channel 19



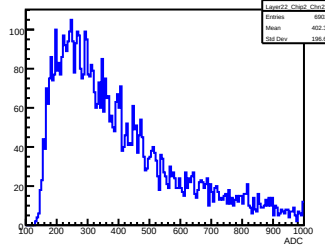
Layer 22, Chip 2, Channel 20



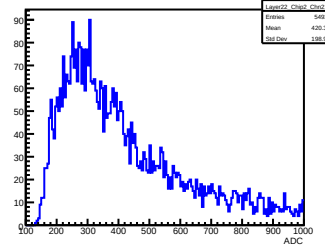
Layer 22, Chip 2, Channel 21



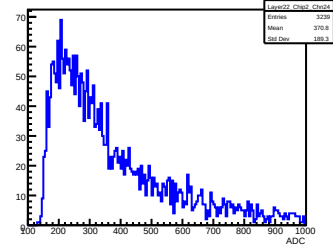
Layer 22, Chip 2, Channel 22



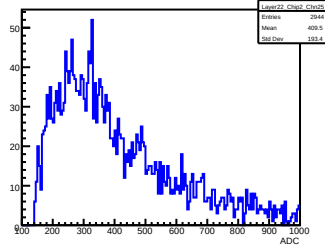
Layer 22, Chip 2, Channel 23



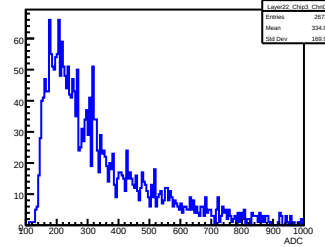
Layer 22, Chip 2, Channel 24



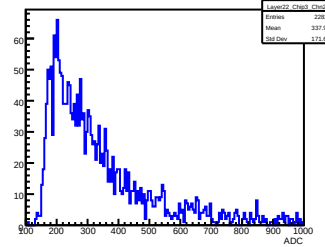
Layer 22, Chip 2, Channel 25



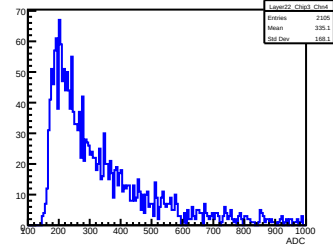
Layer 22, Chip 3, Channel 0



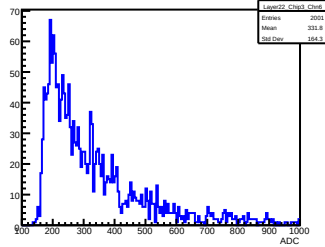
Layer 22, Chip 3, Channel 2



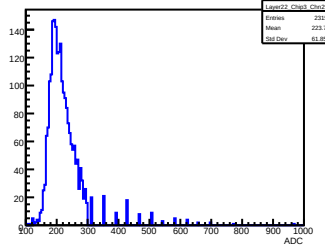
Layer 22, Chip 3, Channel 4



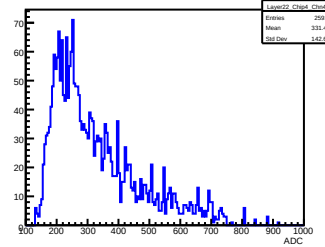
Layer 22, Chip 3, Channel 6



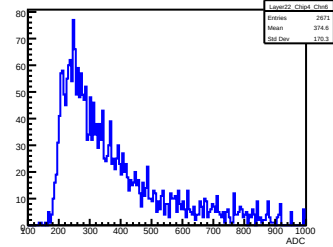
Layer 22, Chip 3, Channel 29



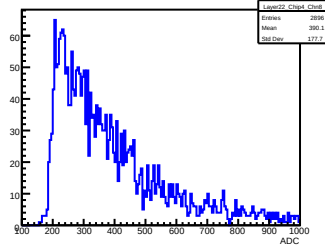
Layer 22, Chip 4, Channel 4



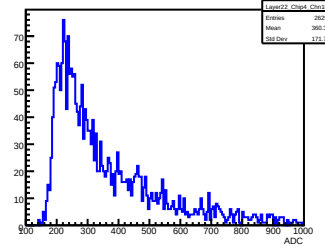
Layer 22, Chip 4, Channel 6



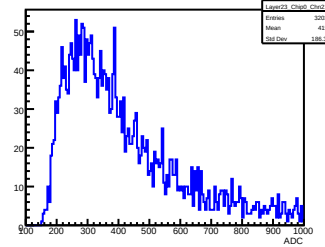
Layer 22, Chip 4, Channel 8



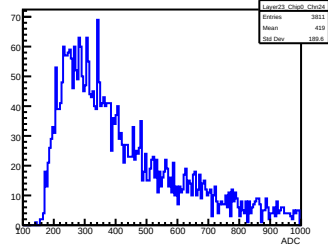
Layer 22, Chip 4, Channel 10



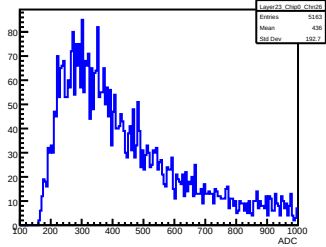
Layer 23, Chip 0, Channel 22



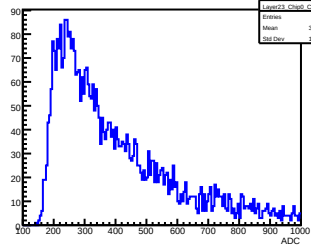
Layer 23, Chip 0, Channel 24



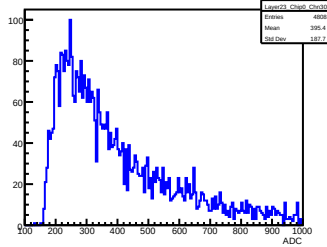
Layer 23, Chip 0, Channel 26



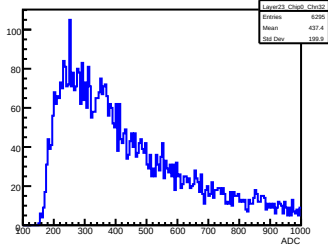
Layer 23, Chip 0, Channel 28



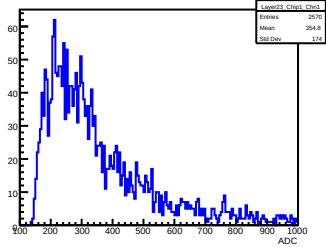
Layer 23, Chip 0, Channel 30



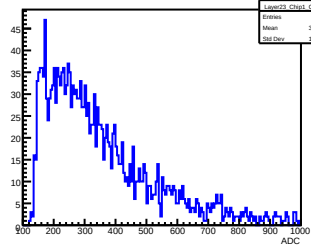
Layer 23, Chip 0, Channel 32



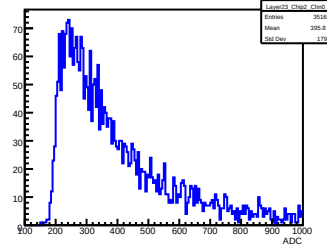
Layer 23, Chip 1, Channel 1



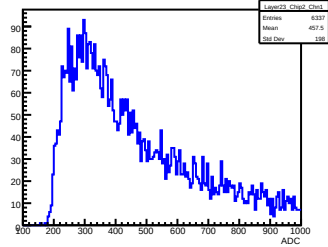
Layer 23, Chip 1, Channel 3



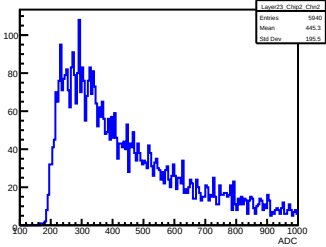
Layer 23, Chip 2, Channel 0



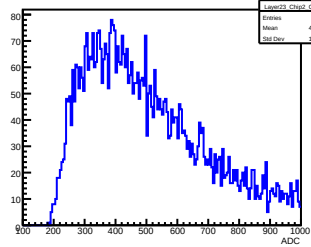
Layer 23, Chip 2, Channel 1



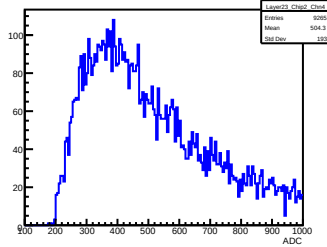
Layer 23, Chip 2, Channel 2



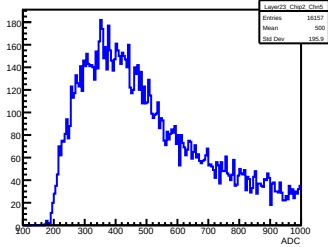
Layer 23, Chip 2, Channel 3



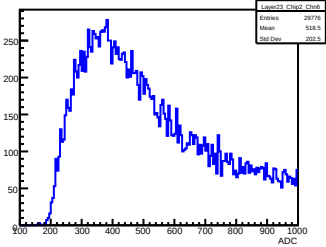
Layer 23, Chip 2, Channel 4



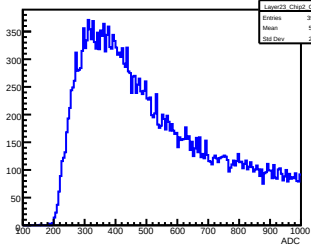
Layer 23, Chip 2, Channel 5



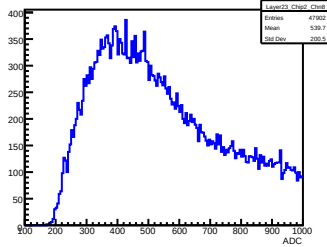
Layer 23, Chip 2, Channel 6



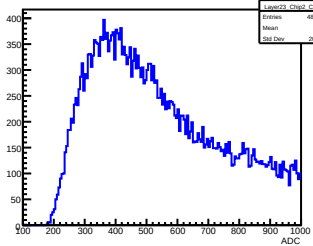
Layer 23, Chip 2, Channel 7



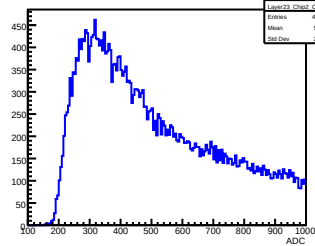
Layer 23, Chip 2, Channel 8



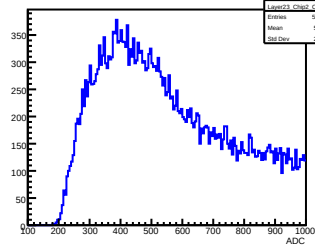
Layer 23, Chip 2, Channel 9



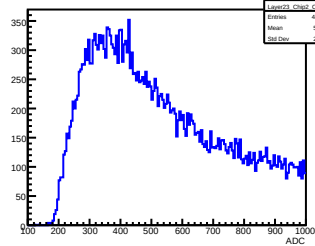
Layer 23, Chip 2, Channel 10



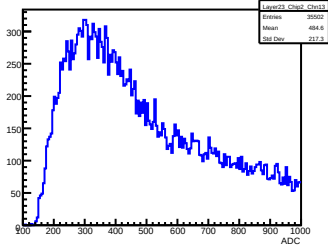
Layer 23, Chip 2, Channel 11



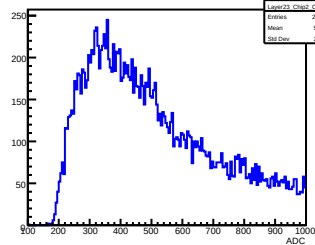
Layer 23, Chip 2, Channel 12



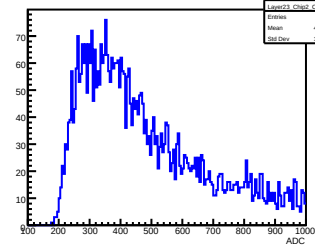
Layer 23, Chip 2, Channel 13



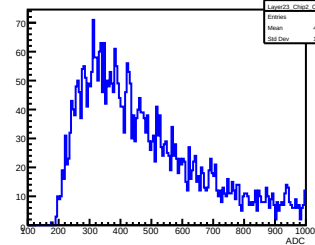
Layer 23, Chip 2, Channel 14



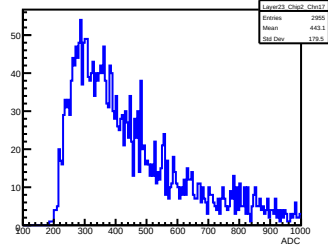
Layer 23, Chip 2, Channel 15



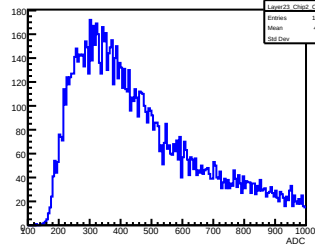
Layer 23, Chip 2, Channel 16



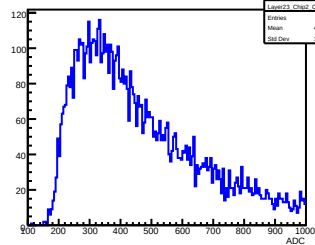
Layer 23, Chip 2, Channel 17



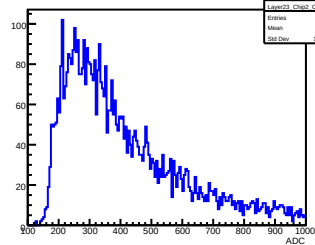
Layer 23, Chip 2, Channel 18



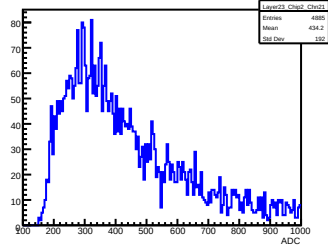
Layer 23, Chip 2, Channel 19



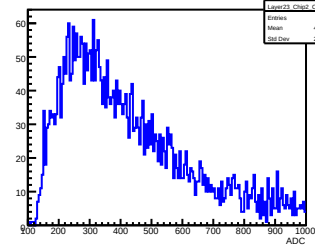
Layer 23, Chip 2, Channel 20



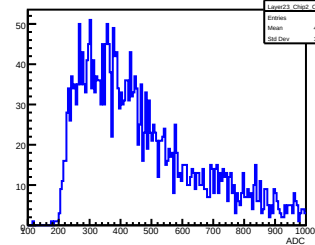
Layer 23, Chip 2, Channel 21



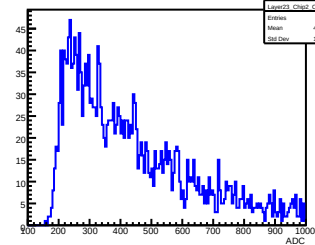
Layer 23, Chip 2, Channel 22



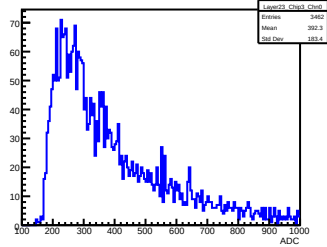
Layer 23, Chip 2, Channel 23



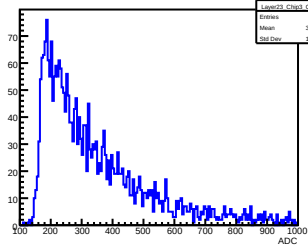
Layer 23, Chip 2, Channel 24



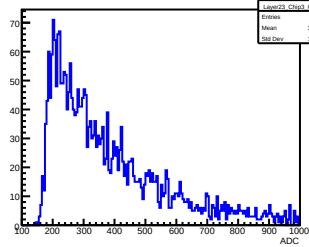
Layer 23, Chip 3, Channel 0



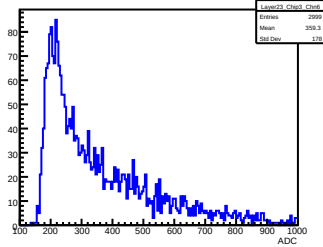
Layer 23, Chip 3, Channel 2



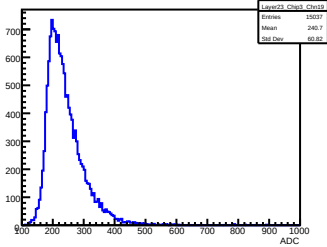
Layer 23, Chip 3, Channel 4



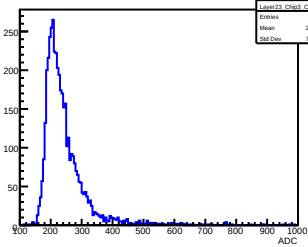
Layer 23, Chip 3, Channel 6



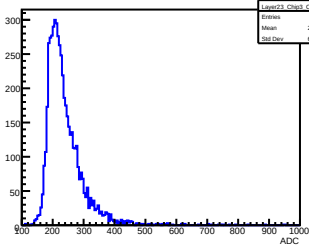
Layer 23, Chip 3, Channel 19



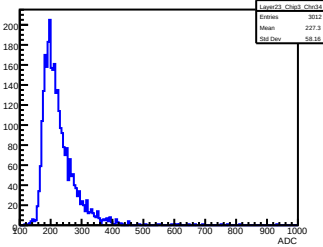
Layer 23, Chip 3, Channel 20



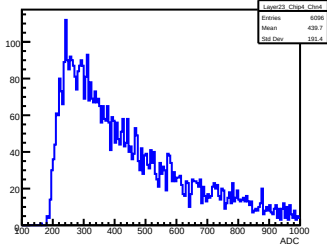
Layer 23, Chip 3, Channel 21



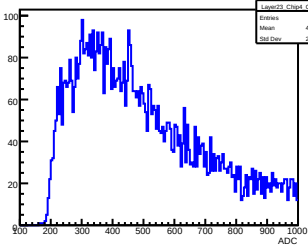
Layer 23, Chip 3, Channel 34



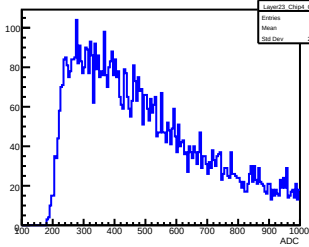
Layer 23, Chip 4, Channel 4



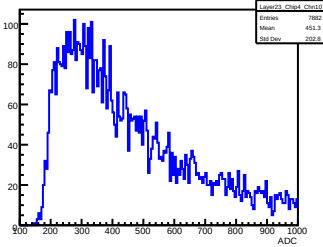
Layer 23, Chip 4, Channel 6



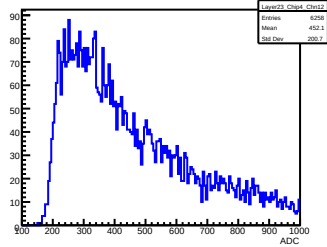
Layer 23, Chip 4, Channel 8



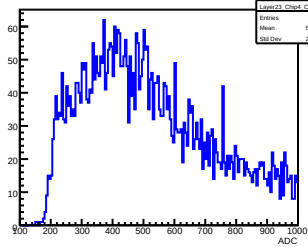
Layer 23, Chip 4, Channel 10



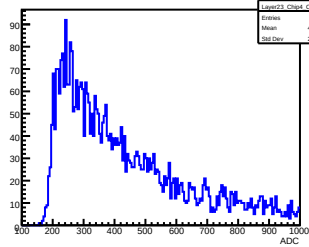
Layer 23, Chip 4, Channel 12



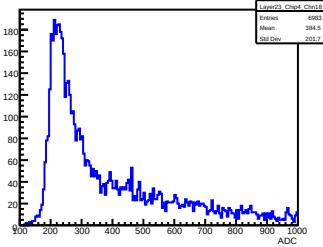
Layer 23, Chip 4, Channel 14



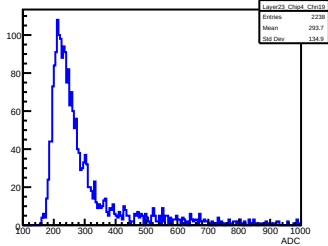
Layer 23, Chip 4, Channel 16



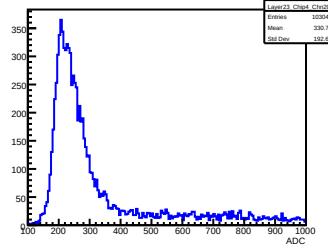
Layer 23, Chip 4, Channel 18



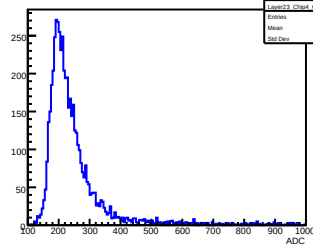
Layer 23, Chip 4, Channel 19



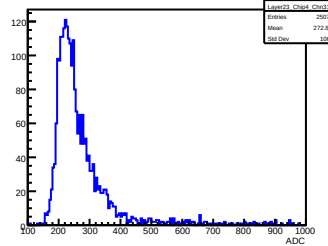
Layer 23, Chip 4, Channel 20



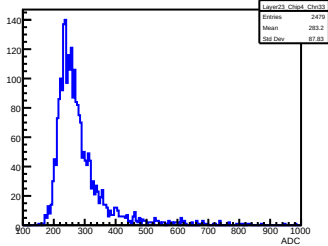
Layer 23, Chip 4, Channel 21



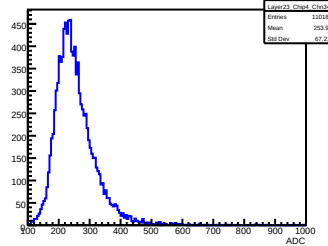
Layer 23, Chip 4, Channel 31



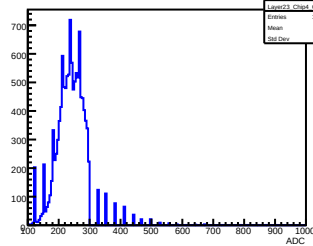
Layer 23, Chip 4, Channel 33



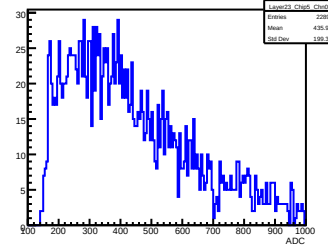
Layer 23, Chip 4, Channel 34



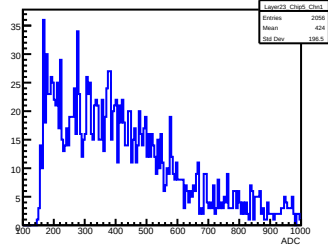
Layer 23, Chip 4, Channel 35



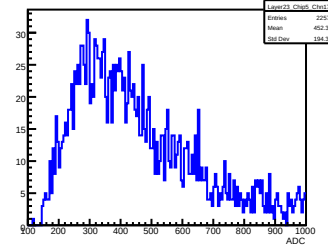
Layer 23, Chip 5, Channel 0



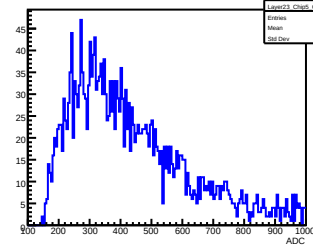
Layer 23, Chip 5, Channel 1



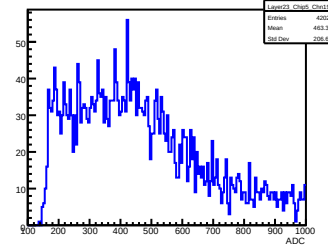
Layer 23, Chip 5, Channel 17



Layer 23, Chip 5, Channel 18



Layer 23, Chip 5, Channel 19



Layer 23, Chip 5, Channel 20

