

Saturday 25 May 2024

Session 3 (New structures,new detector technologies) (08:25 - 12:20)

-Conveners: ;

time	[id] title	presenter
08:25	[29] 55nm CMOS	,
08:50	[72] CMOS	,
09:03	[76] X	,
09:16	[25] Status of the 3D silicon sensor R&D at USTC	(MA), (Kuo)
09:29	[33]	,
09:42	[47] LGAD Based 4D Outer time-Tracker Development for CEPC	LI, Meng zhao
09:55	[16] 4H-SiC	,
10:08	[75] alpha	,
10:21	[46] SDD	BAO, Ziyu
10:34		
10:49	[24] 3D-SOI	, ,
11:02	[45] BCPS	,
11:15	[54] X	,
11:28	[41]	,
11:41	[58]	ZHANG, Haolin
11:54	[60] Test of CMOS chip using 55nm process	,
12:07	[55] RASER	,