Friday 24 May 2024

Session 1 (Electronics) (08:15 - 12:25)

-Conveners: ;

time	[id] title	presenter
08:15	□□□□	
08:20	[61] □□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□	□, □
08:45	[49] □□□STCF□□□□□□□□MAPS□□□□	□, □□
08:58	[19] □□□□□□CZT□□□□□□□□□□□	□, □□
09:11	[74] Timepix4□□□□□□□□□□□□□□□□□□□	LIU, Hongbin
09:24	[56] □□□□ DSM □□□□□□ SiPM □□□□	DENG, YunQi
09:37	[48] HERD□□□□□□□□□□□□□□□□□□□□	□, □
09:50	[44] □□FPGA□□□□ADC□□□□	□, □
10:03	[69] □□CZT□□□□128□□□□□□□□□□□ASIC□□	□, □
10:16	[68] □□□□□□□□□□□□□CMOS□□□□□□□□	□, □□
10:29	□□□□□	
10:54	[22] □□ VLAST □□□□□□□□□□□□□□□□□□ ASIC	Dr □, □
11:07	[26] HERD□□□□□□□□□□□□□□	□, □
11:20	[11] □□□□□□□□□□□□□□□□□□□□HYLITE200F□□□□□□□□□□□□□□	JIA, Xining
11:33	[51] □□□□□□□□□□□□□□CPRE_1CH □□□□□□	□, □
11:46	[70] □□□□□□□□□□□□SiPM&LGAD□□□□□□□□□□□	□, □□
11:59	[71] □□□□□□□□□□□□SHA-less Pipelined ADC□□	Mr □, □□
12:12	[36] □□□TOF-PET□□□□□□□□□□□□ASIC□□	Dr □, □